RoHS

COMPLIANT HALOGEN

FREE

**Vishay Semiconductors** 

High Performance Schottky Rectifier, 1.0 A



Cathode	Anode
0	0

SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.0 A			
V <sub>R</sub>	60 V			
V <sub>F</sub> at I <sub>F</sub>	0.42 V			
I <sub>RM</sub>	8 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
E <sub>AS</sub>	2.0 mJ			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

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### **FEATURES**

- · Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

The VS-10BQ060-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS VALUES UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	1.0	А		
V <sub>RRM</sub>		60	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	А		
V <sub>F</sub>	1.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.42	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10BQ060-M3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	60	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 116 °C, rectangular waveform		1.0	А
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	700	
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	42	A
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 4 mH		2.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1		1 A	T 05 00	0.49	V
	V (1)	2 A	T <sub>J</sub> = 25 °C	0.60	
	V <sub>FM</sub> <sup>(1)</sup>	1 A	- T <sub>J</sub> = 125 °C	0.42	
		2 A		0.56	
Maximum reverse leakage current		T <sub>J</sub> = 25 °C	V Deted V	0.1	mA
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	8.0	
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		80	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of charge	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

Note

 $^{(1)}\,$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>		80	C/W
Approximate weight			0.10	g
			0.003	oz.
Marking device		Case style SMB (DO-214AA)	11	4

#### Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

(2) Mounted 1" square PCB



VS-10BQ060-M3

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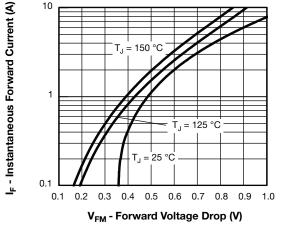


Fig. 1 - Maximum Forward Voltage Drop Characteristics

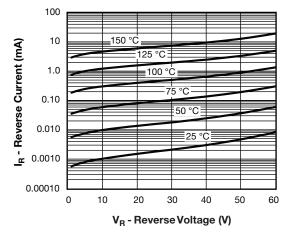


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

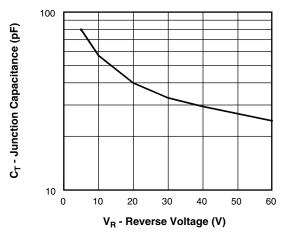


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

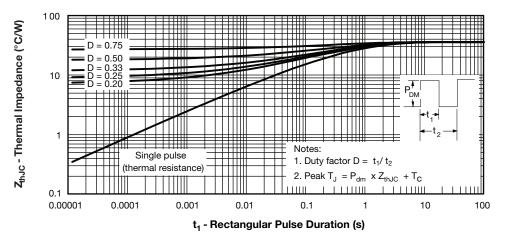
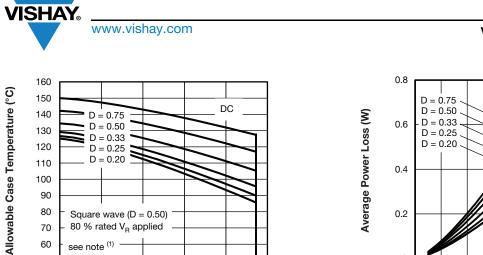


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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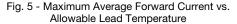
I<sub>F(AV)</sub> - Average Forward Current (A)

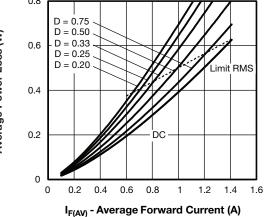
0.9

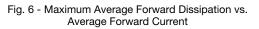
1.2

1.5

0.6







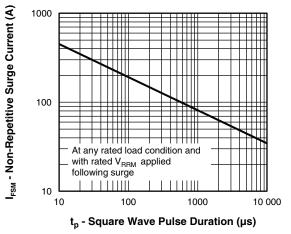


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;
  - Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6);  $Pd_{REV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$

# VS-10BQ060-M3

### **Vishay Semiconductors**

60

50

0.0

see note <sup>(1)</sup>

0.3

## ORDERING INFORMATION TABLE

SHAY

Device code	VS-	10	в	Q	060	-M3
		2	3	4	5	6
	1 -	Visl	hay Sen	niconduc	ctors pro	oduct
	2 -	Cur	rent rati	ng		
	3 -	В=	SMB			
	4 -	Q =	Schottk	ky "Q" se	eries	
	5 -	Vol	tage rati	ng (060	= 60 V)	)
	6 -	Env	vironmer	ntal digit	:	
		-M3	- haloc	on_froo	Rous	complia

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-10BQ060-M3/5BT	5BT	3200	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95401
Part marking information	www.vishay.com/doc?95403
Packaging information	www.vishay.com/doc?95404
SPICE model	www.vishay.com/doc?95638

## VS-10BQ060-M3

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