



PRODUCT DATA SHEET



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Datasheet

ces Sami

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

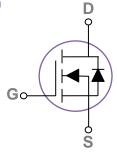
BVDSS	RDSON	ID
20V	23m Ω	4A

Features

- 20V, 4A, $RDS(ON) = 23m\Omega@VGS = 4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

SOT-23 Pin Configuration





Applications

- Notebook
- Load Switch
- Hend-Held Instruments

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
Vgs	Gate-Source Voltage	±12	V
	Drain Current – Continuous (Tc=25°C)	4	А
D	Drain Current – Continuous (T _C =100°C)	3.2	А
DM	Drain Current – Pulsed1	20	А
<u> </u>	Power Dissipation (Tc=25°C)	1.56	W
o _D	Power Dissipation – Derate above 25°C	0.012	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 125	°C

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
Reja	Thermal Resistance Junction to ambient		80	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	20			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.02		V/°C
I _{DSS} Dra	Drain Source Leekens Current	V _{DS} =20V , V _{GS} =0V , T _J =25°C			1	uA
	Drain-Source Leakage Current	V _{DS} =16V , V _{GS} =0V , T _J =125°C			10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V , V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistance	Static Drain Source On Registance	V _{GS} =4.5V , I _D =4A		23	40	mΩ
	Static Drain-Source On-Nesistance	V _{GS} =2.5V , I _D =3A		30	55	11.22
$V_{GS(th)}$	Gate Threshold Voltage	V V I- 250uA	0.4	0.6	1	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA		2		mV/°C
gfs	Forward Transconductance	V _{DS} =10V , I _S =2A		4.4		S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}		 5.8	
Q_{gs}	Gate-Source Charge ^{2,3}	V_{DS} =10V , V_{GS} =4.5V , I_{D} =4A	 0.6	nC
Q_{gd}	Gate-Drain Charge ^{2,3}		 1.5	
$T_{d(on)}$	Turn-On Delay Time ^{2,3}		 2.9	
Tr	Rise Time ^{2, 3}	V_{DD} =10 V , V_{GS} =4.5 V , R_{G} =25 Ω	 8.4	nS
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}	I _D =1A	 19.2	113
Tf	Fall Time ^{2,3}		 5.6	
Ciss	Input Capacitance		 315	
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , F=1MHz	 50	рF
Crss	Reverse Transfer Capacitance		 40	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			4	Α
Isм	Pulsed Source Current	VG=VD=UV, Force Current			8	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25°C			1.2	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
 Essentially independent of operating temperature.



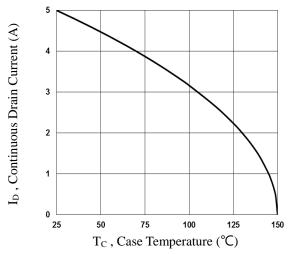


Fig.1 Continuous Drain Current vs. Tc

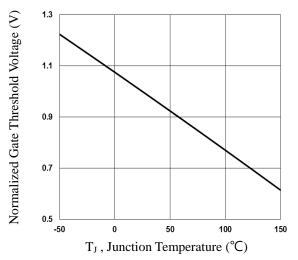


Fig.3 Normalized V_{th} vs. T_J

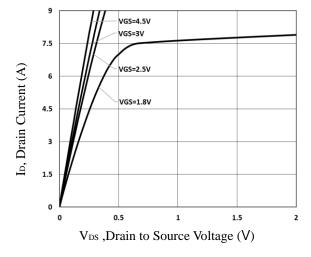


Fig.5 Typical Output Characteristics

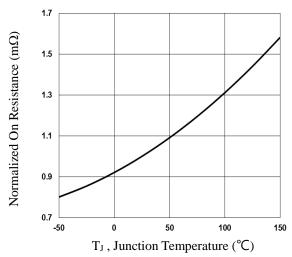


Fig.2 Normalized RDSON vs. TJ

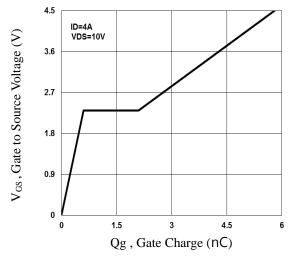


Fig.4 Gate Charge Waveform

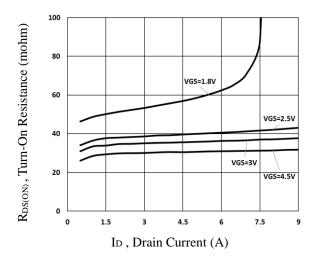


Fig.6 Turn-On Resistance vs. ID



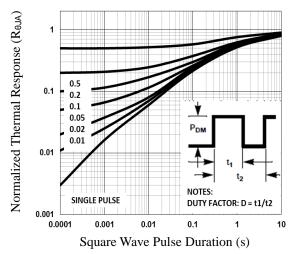


Fig.7 Normalized Transient Impedance

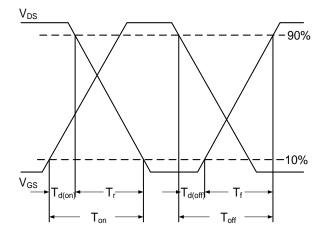


Fig.9 Switching Time Waveform

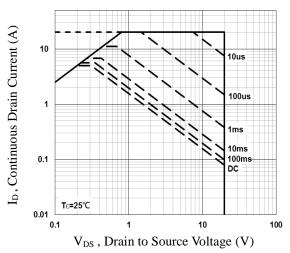


Fig.8 Maximum Safe Operation Area

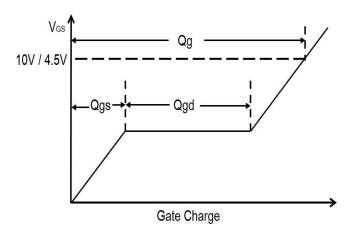
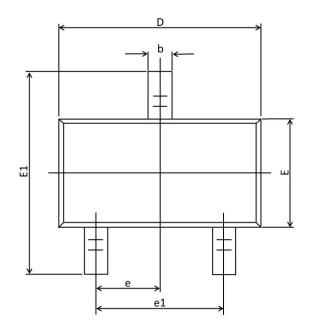
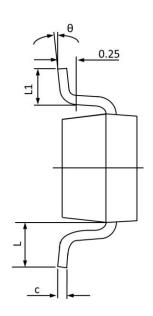


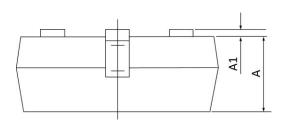
Fig.10 Gate Charge Waveform



SOT-23 PACKAGE INFORMATION







Crumbal	Dimensions I	n Millimeters	Dimension	s In Inches	
Symbol	Min	Max	Min	Max	
A	0.900	1.000	0.035	0.039	
A1	0.000	0.100	0.000	0.004	
b	0.300	0.500	0.012	0.020	
С	0.090	0.110	0.003	0.004	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E 1	2.250	2.550	0.089	0.100	
e	0.950 TYP.		0.037	TYP.	
e1	1.800	2.000	0.071	0.079	
L	0.550	REF.	0.022 REF.		
L1	0.300	0.500	0.012	0.020	
θ	1°	7°	1°	7°	



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