



**Features**

- Radial Leaded Devices
- Cured, flame, retardant epoxy polymer insulating material meets UL 94V-0 requirements
- Bulk packaging, or tape and reel available on most models

**Applications**

- Almost anywhere there is a low voltage power supply, up to 16V and a load to be protected, including:
- Personal computer
  - Medical electronics
  - Personal care product

Model	V <sub>max</sub> (Vdc)	I <sub>max</sub> (A)	I <sub>hold</sub> (A)	I <sub>trip</sub> (A)	P <sub>d</sub> Typ. (W)	Maximum Time To Trip		Resistance		Agency Approval	
						Current (A)	Time (Sec)	Ri min (Ω)	R1 max (Ω)	UL	TUV
Bp900-016	16	100	0.90	1.80	0.60	8.00	1.2	0.070	0.180		
BpA01.10-016	16	100	1.10	2.20	0.70	8.00	2.3	0.050	0.140		
BpA01.35-016	16	100	1.35	2.70	0.80	8.00	4.5	0.040	0.120		
BpA01.60-016	16	100	1.60	3.20	0.90	8.00	9.0	0.030	0.110		
BpA01.85-016	16	100	1.85	3.70	1.00	8.00	10.0	0.030	0.090		
BpA02.50-016	16	100	2.50	5.00	1.20	12.50	5.0	0.020	0.060		
BpA03.00-016	16	100	3.00	5.10	2.30	9.00	10.0	0.030	0.095		
BpA04.00-016	16	100	4.00	6.80	2.40	12.00	10.0	0.018	0.043		
BpA05.00-016	16	100	5.00	8.50	2.60	15.00	10.0	0.014	0.030	✓	
BpA06.00-016	16	100	6.00	10.20	2.80	18.00	10.0	0.009	0.025	✓	
BpA07.00-016	16	100	7.00	11.90	3.00	21.00	10.0	0.008	0.019	✓	
BpA08.00-016	16	100	8.00	13.60	3.00	24.00	10.0	0.006	0.016	✓	
BpA09.00-016	16	100	9.00	15.30	3.30	27.00	10.0	0.005	0.012	✓	
BpA10.00-016	16	100	10.00	17.00	3.60	30.00	30.0	0.0045	0.011	✓	
BpA11.00-016	16	100	11.00	18.70	3.70	33.00	30.0	0.004	0.010	✓	
BpA12.00-016	16	100	12.00	20.40	4.20	36.00	30.0	0.0035	0.009	✓	
BpA13.00-016	16	100	13.00	23.00	4.40	65.00	8.5	0.002	0.009	✓	
BpA14.00-016	16	100	14.00	23.80	4.60	70.00	30.0	0.002	0.008	✓	
BpA15.00-016	16	100	15.00	25.50	4.80	75.00	9.5	0.002	0.008	✓	
BpA18.00-016	16	100	18.00	30.40	5.10	54.00	30.0	0.0026	0.008		

**I<sub>hold</sub>** = Hold Current : maximum current device will sustain for 4 hours without tripping in 25°C still air.

**I<sub>trip</sub>** = Trip Current : minimum current at which the device will trip in 25°C still air.

**V<sub>max</sub>** = Maximum voltage device can withstand without damage at rated current (I<sub>max</sub>).

**I<sub>max</sub>** = Maximum fault current device can withstand without damage at rated voltage (V<sub>max</sub>).

**P<sub>d</sub>** = Power dissipated from device when in the tripped state at 25°C still air.

**Ri min/max** = Minimum/Maximum resistance of device in initial (un-soldered) state.

**R1 max** = Maximum resistance of device at 25°C measured one hour after tripping.

**CAUTION : Operation beyond the specified ratings may result in damage and possible arcing and flame.**

**Environmental Specifications**

Test	Conditions	Resistance change
Passive aging	+85°C, 1000 hrs.	±5% typical
Humidity aging	+85°C, 85% R.H., 1000 hrs	±5% typical
Thermal shock	+85°C to -40°C, 20 times	±10% typical
Resistance to solvent	MIL-STD-202, Method 215	No change
Vibration	MIL-STD-202, Method 201	No change
Ambient operating /storage conditions : - 40 °C to +85 °C		
Maximum surface temperature of the device in the tripped state is 125 °C		

**Agency Approvals :**



**Regulation/Standard:**



2002/95/EC



EN14582

**PHYSICAL SPECIFICATIONS :**

**Materials : Leads**

Bp900-016 ~ BpA02.50-016 : Tin plated copper-clad steel, 24 AWG (0.51mm/0.020" Dia.)

BpA03.00-016 ~ BpA16.00-016 : Tin plated copper, 20 AWG (0.81mm/0.032" Dia.)

**Lead Solderability :** MIL-STD-202, Method 208E

**Device Labeling :** Device is marked with Logo, amperage rating, voltage rating & date code.

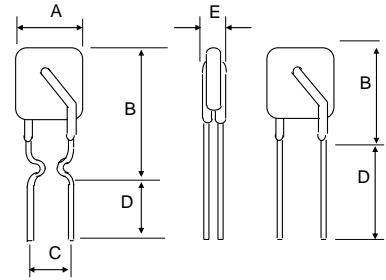


**WARNING:**

- Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- Use PPTC with a large inductance in circuit will generate a circuit voltage (L di/dt) above the rated voltage of the PPTC.
- Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.

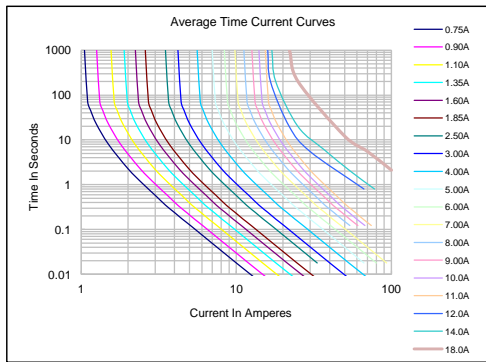
**Physical Dimensions (Unit: mm)**

Model	A	B	C	D	E	Lead Style
	Max.	Max.	Typ.	Min.	Max.	
Bp900-016	7.40	12.20	5.10	7.6	3.1	Kink
BpA01.10-016	7.40	14.20	5.10	7.6	3.1	Kink
BpA01.35-016	8.90	13.50	5.10	7.6	3.1	Kink
BpA01.60-016	8.90	15.20	5.10	7.6	3.1	Kink
BpA01.85-016	10.20	15.70	5.10	7.6	3.1	Kink
BpA02.50-016	10.40	14.30	5.10	7.6	3.1	Kink
BpA03.00-016	8.80	11.80	5.10	7.6	3.1	Straight
BpA04.00-016	9.50	12.50	5.10	7.6	3.1	Straight
BpA05.00-016	9.80	14.60	5.10	7.6	3.1	Straight
BpA06.00-016	11.60	14.60	5.10	7.6	3.1	Straight
BpA07.00-016	13.00	17.20	5.10	7.6	3.1	Straight
BpA08.00-016	14.50	20.00	5.10	7.6	3.1	Straight
BpA09.00-016	14.50	20.00	5.10	7.6	3.1	Straight
BpA10.00-016	17.50	24.50	10.20	7.6	3.1	Straight
BpA11.00-016	17.50	24.50	10.20	7.6	3.1	Straight
BpA12.00-016	17.50	24.50	10.20	7.6	3.5	Straight
BpA13.00-016	23.50	27.90	10.20	7.6	3.5	Straight
BpA14.00-016	20.50	28.00	10.20	7.6	3.5	Straight
BpA15.00-016	24.10	28.70	10.20	7.6	3.5	Straight
BpA18.00-016	25.70	32.30	10.20	7.6	3.5	Straight



Note : Stand-offs only used for Bp900-016 – BpA02.50-016

**Typical Time-To-Trip Curve at 25°C**



**Thermal Derating Curve**

