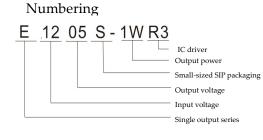
Luoding Ruilvte Electronic Technology Co., Ltd.

DC-DC Power Supply Module/3000V Isolation Fixed Voltage Input/Unregulated DualOutput/1WR3

11t E05055-1WR3 F045 R045 TTT



DC/DC Converters

Product features: Isolation voltage: 3000Vdc isolation Operating temperature: -45℃-85℃ Stable performance, high reliability MTBF≥2 million hours

Flame-retardant packaging Meeting UL94-V0 requirements International standard pinout (Pin 1/2/5/6/7)

Surface-mount design

No additional components required Compliant with the RoHS Directive

Module selection guide						
	Input		Output			Conversion efficiency
Model number	Nominal voltage (V)	Voltage Range (V)	Rated voltage (V)	Minimum Current (mA)	Maximum current (mA)	(%)
E0503S-1WR3		4.5-5.5	±3.3	± 15	±150	76
E0505S-1WR3			± 5	± 10	±100	81
E0509S-1WR3	- 5		±9	± 6	±55	82
E0512S-1WR3			±12	±4	±42	81
E0515S-1WR3			±15	±3	± 33	82
E0524S-1WR3			±24	± 2	± 21	80
E1203S-1WR3	12		±3.3	± 15	± 150	76
E1205S-1WR3			± 5	± 10	± 100	79
E1209S-1WR3		10.8-13.2	±9	±6	± 55	80
E1212S-1WR3			±12	±4	±42	82
E1215S-1WR3			±15	± 3	±33	82
E1224S-1WR3			±24	±2	± 21	80
E1503S-1WR3	15		±3.3	± 15	± 150	76
E1505S-1WR3			± 5	± 10	± 100	78
E1509S-1WR3		13.5-16.5	±9	±6	± 55	79
E1512S-1WR3		13.3-10.3	±12	± 4	±42	80
E1515S-1WR3			± 15	± 3	±33	80
E1524S-1WR3			±24	±2	± 21	80
E****S-1WR3 * Tailored model based on client needs. *						

We reserve the right to change the above parameters. Final product specifications will be according to the specific product datasheet provided by our company.

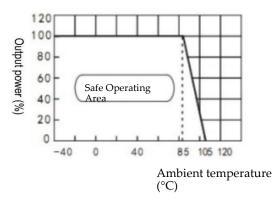
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General characteristics				
Switching frequency	100KHz		100% load, nominal input voltage	
Output short-circuit duration			Long duration, resettable	
Casing's temperature rise during	15°С (Тур.)		25°C (Max)	
operation				
Temperature coefficient	0.03%/°C		100% full load	
Pin soldering temperature	300℃		Soldering time≤3s	
Isolation voltage (input and	3000VDC		Test time: 1 minute	
output)			Leakage current: less than 1mA	
Insulation resistance	1000ΜΩ		Insulation voltage: 500V	
Operating temperature	-40~+85℃		Operating ambient temperature	
Storage temperature $-55 \sim +125^{\circ} C$				
Storage humidity <95%			Non-condensing	
Cooling method Natural air cooling		g		
Weight	SIP series: 1.2g		Standard	
Input characteristics				
Voltage range		≤±10%		
Filtering	Ceramic capa		citor	
No-load power consumption	10% rated pov		wer (typical value)	
Output characteristics				
Item Valu		le	Test conditions	
Linear voltage regulation rate ±1.2 (Max)			Input voltage variation 1%	
Load regulation ≤±10% (Typ); ±15		% (Max)	10% to 100% load	
Output voltage accuracy	Please refer to the Envelope		100% full load	
-	Curve for Errors			
Ripple and noise≤75mVp-p (Typ)100mVp-p (Max)			Bandwidth: 20MHz	

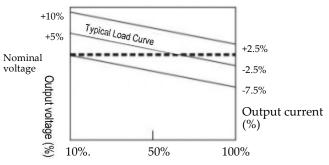
Unless otherwise specified, all parameters are tested under nominal input voltage, resistive load, and at room temperature of 25°C.

Curves for typical characteristics

Temperature Curve



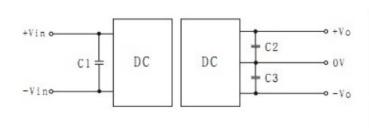
Envelope Curve for Errors



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Recommended circuit for basic application



Capacitive load table:

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Input	External	Output	External			
voltage	capacitor	voltage	capacitor			
(VDC)	(uF)	(VDC)	(uF)			
3.3 or 5	4.7	±3.3 or ±5	10			
12	2.2	±9	4.7			
15or24	1	±12	2.2			
		±15or±24	1 or 0.47			

Caution

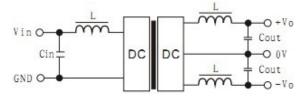
1. Output load requirements: Avoid no-load operation. When the actual power consumption of the load is less than 10% of the module's rated output power or if there is a no-load condition, it is recommended to connect a dummy load at the output end or choose a module with a smaller rated power. The dummy load (resistor) can be calculated as 5-10% of the module's rated power. Value of the resistance = $U2 / (10\% \times 1WR3)$.

2. Overload protection: Under normal operating conditions, the output circuit of this product has no protection against overload conditions. The simplest method is to connect a resettable fuse in series at the input end or to add a circuit breaker to the circuit.

3. The capacitance of the external capacitor at the output end should not be too large; otherwise, it may cause overcurrent or poor startup during module initiation. The specific value of the capacitance should be according to the capacitive load table.

4. For applications with high ripple and noise requirements, an external LC filter circuit should be used (as shown in Figure 1). It is recommended to use ceramic capacitors or high-frequency low-impedance electrolytic capacitors for Cout. Using tantalum capacitors may cause module damage.

5. The simplest method for output voltage regulation, overvoltage protection, and overcurrent protection is to connect a linear regulator with over temperature protection in series at the input or output end (as shown in Figure 2).





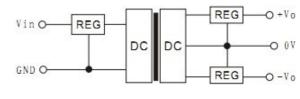


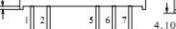
Figure 2

Recommended PCB layout:

	-	F	F	-	÷ -		-
	1	2			5	6	7
L	_			_	L -		1

Top view		
Grid: 2.54mm	Hole diameter: 1.00mm	L

E****S-1WR3						
Pin	1	2	5	6	7	
Function	+Vin	-Vin	-Vo	0 V	+Vo	
Description	Negative input	Positive input	-Output	Ground	+Output	



Front view

Dimensions and pinout

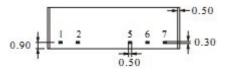
19.6

9.5

2.54

0.5

Side view



Bottom view

(Unit: mm Tolerance: ±0.25)

Silkscreen

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