Luoding Ruilvte Electronic Technology Co., Ltd.

DC/DC Converters

H_S-2WR3 Series

DC-DC Power Supply Module/3000V Isolation Fixed Voltage Input/Unregulated Single Output/2WR3 Product features:

Isolation voltage: 6000Vdc isolation Operating temperature: -45°C-85°C Stable performance, high reliability MTBF≥2 million hours Flame-retardant packaging

Meeting UL94-V0 requirements International standard pinout (Pin 1/2/5/7)

Surface-mount design

No additional components required Compliant with the RoHS Directive

Module selection guide						
	Input		Output			Conversion efficiency
Model number	Nominal	Voltage	Rated	Minimum	Maximum	
	voltage	Range	voltage	Current	current	(%)
	(V)	(V)	(V)	(mA)	(mA)	
H0503S-2WR3		4.5-5.5	3.3	66	606	76
H0505S-2WR3			5	40	400	81
H0509S-2WR3	F		9	22	222	82
H0512S-2WR3	5		12	16	166	81
H0515S-2WR3			15	13	133	82
H0524S-2WR3			24	8	84	80
H1203S-2WR3		10.8-13.2	3.3	66	606	76
H1205S-2WR3			5	40	400	79
H1209S-2WR3			9	22	222	80
H1212S-2WR3	12		12	16	166	82
H1215S-2WR3			15	13	133	82
H1224S-2WR3			24	8	84	80
H2403S-2WR3	24		3.3	66	303	76
H2405S-2WR3		21.6-26.4	5	40	200	78
H2409S-2WR3			9	22	111	79
H2412S-2WR3			12	16	166	80
H2415S-2WR3			15	13	133	80
H2424S-2WR3			24	8	84	80
H****S-2WR3	* Tailored model based on client needs. *					

General characteristics				
Switching frequency	100KHz	100% load, nominal input voltage		
Output short-circuit duration	1s	Additional short-circuit protection		
		feature, indicated by the letter "R"		
		after the model number.		
Casing's temperature rise during	15℃ (Typ.)	25℃ (Max)		
operation				
Temperature coefficient	0.03%/℃	100% full load		
Pin soldering temperature	300℃	Soldering time≤3s		
Isolation voltage (input and	6000VDC	Test time: 1 minute		
output)		Leakage current: less than 1mA		
Insulation resistance	1000ΜΩ	Insulation voltage: 500V		
Operating temperature	-40∼+85°C	Operating ambient temperature		
Storage temperature	-55~+125℃			
Storage humidity	<95%	Non-condensing		
Cooling method	Natural air cooling			
Weight	SIP series: 1.2g	Standard		

Input characteristics

Voltage range	≤±10%		
Filtering	Ceramic capacitor		
No-load power consumption	10% rated power (typical value)		

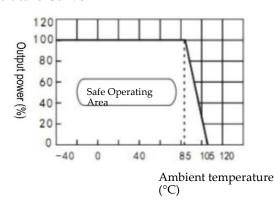
Output characteristics

Item	Value	Test conditions	
Linear voltage regulation rate	±1.2 (Max)	Input voltage variation 1%	
Load regulation	≤±10% (Typ); ±15% (Max)	10% to 100% load	
Output voltage accuracy	Please refer to the Envelope	100% full load	
	Curve for Errors		
Ripple and noise	≤75mVp-p (Typ)	Bandwidth: 20MHz	
	100mVp-p (Max)		

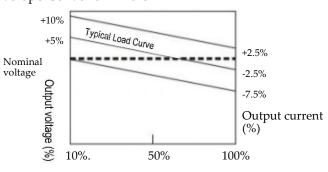
Unless otherwise specified, all parameters are tested under nominal input voltage, resistive load, and at room temperature of 25°C.

Curves for typical characteristics

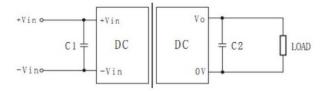
Temperature Curve



Envelope Curve for Errors



Recommended circuit for basic application

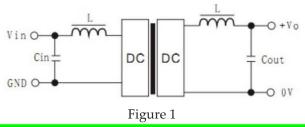


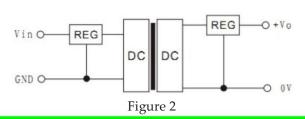
Capacitive load table:

capacitive load table.				
Input	External	Output	External	
voltage	capacitor	voltage	capacitor	
(VDC)	(uF)	(VDC)	(uF)	
3.3 or 5	4.7	3.3 or 5	10	
12	2.2	9	4.7	
15or24	1	12	2.2	
		15or24	1 or 0.47	

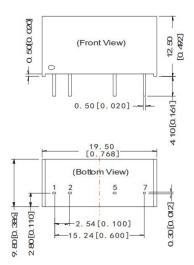
Caution

- 1. Output load requirements: Avoid no-load operation. When the actual power consumption of the load is less than 10% of the module's rated output power or if there is a no-load condition, it is recommended to connect a dummy load at the output end or choose a module with a smaller rated power. The dummy load (resistor) can be calculated as 5-10% of the module's rated power. Value of the resistance = $U2 / (10\% \times 2W)$.
- 2. Overload protection: Under normal operating conditions, the output circuit of this product has no protection against overload conditions. The simplest method is to connect a resettable fuse in series at the input end or to add a circuit breaker to the circuit.
- 3. The capacitance of the external capacitor at the output end should not be too large; otherwise, it may cause overcurrent or poor startup during module initiation. The specific value of the capacitance should be according to the capacitive load table.
- 4. For applications with high ripple and noise requirements, an external LC filter circuit should be used (as shown in Figure 1). It is recommended to use ceramic capacitors or high-frequency low-impedance electrolytic capacitors for Cout. Using tantalum capacitors may cause module damage.
- 5. The simplest method for output voltage regulation, overvoltage protection, and overcurrent protection is to connect a linear regulator with over temperature protection in series at the input or output end (as shown in Figure 2).





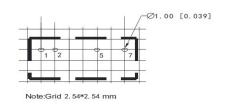
Dimensions and pinout



Bottom view

(Unit: mm Tolerance: ±0.25)

Recommended PCB layout:



Top view

Grid: 2.54mm Hole diameter: 1.00mm

H****S-2WR3					
Pin	1	2	5	7	
Function	-Vin	+Vin	0V	+Vo	
Description	Negative	Positive	Ground	Output	
	input	input			

We reserve the right to change the above parameters. Final product specifications will be according to the specific product datasheet provided by our company.