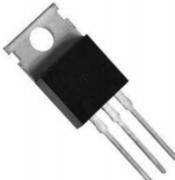
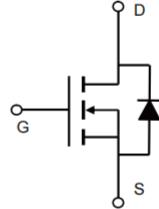


Description

<p>Features</p> <ul style="list-style-type: none"> ● 40V, 120A ● $R_{DS(ON)} < 4.5\text{m}\Omega$ @ $V_{GS} = 10\text{V}$ ● $R_{DS(ON)} < 6.5\text{m}\Omega$ @ $V_{GS} = 4.5\text{V}$ ● Advanced Trench Technology ● Provide Excellent $R_{DS(ON)}$ and Low Gate Charge ● Lead free product is acquired 	<p>Application</p> <ul style="list-style-type: none"> ● Load Switch ● PWM Application ● Power management
 <p>TO-220C</p>	 <p>Schematic Diagram</p>

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		40	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	120	A
		$T_C = 100^\circ\text{C}$	78	A
I_{DM}	Pulsed Drain Current ^{note1}		480	A
EAS	Single Pulsed Avalanche Energy ^{note2}		148	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	123	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		1.2	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}$, $V_{GS}=0\text{V}$,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}$, $V_{GS}= \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1.1	1.6	2.4	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$, $I_D=30\text{A}$	-	3.4	4.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=20\text{A}$	-	4.5	6.5	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=20\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$	-	5595	-	pF
C_{oss}	Output Capacitance		-	411	-	pF
C_{rss}	Reverse Transfer Capacitance		-	340	-	pF
Q_g	Total Gate Charge	$V_{DS}=20\text{V}$, $I_D=30\text{A}$, $V_{GS}=10\text{V}$	-	65	-	nC
Q_{gs}	Gate-Source Charge		-	12.5	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	15	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=20\text{V}$, $I_D=30\text{A}$, $R_L=1\Omega$, $R_{\text{GEN}}=3\Omega$, $V_{GS}=10\text{V}$	-	12	-	ns
t_r	Turn-on Rise Time		-	16	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	39	-	ns
t_f	Turn-off Fall Time		-	15	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	60	-	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	240	-	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=30\text{A}$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$T_J=25^\circ\text{C}$, $I_f=20\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	-	22	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	11	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=20\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{AS}=24.4\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

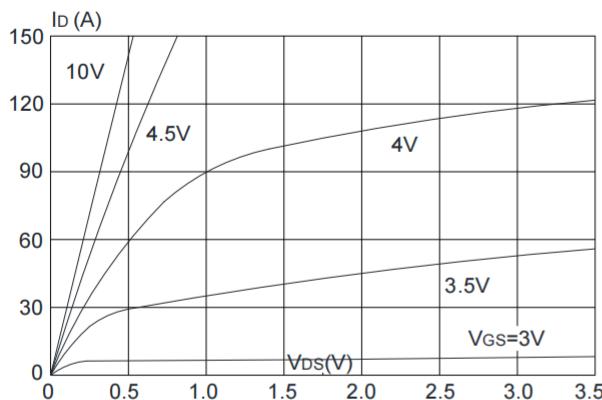


Figure 3: On-resistance vs. Drain Current

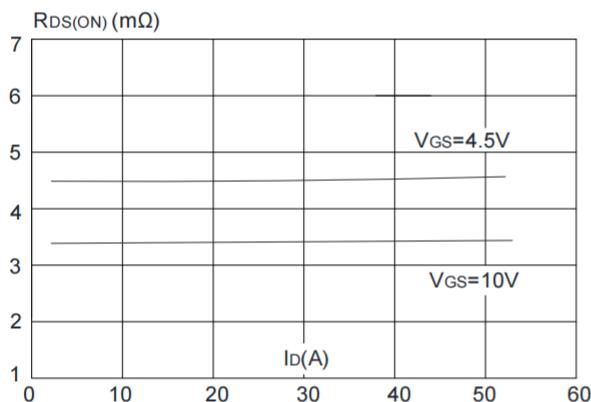


Figure 5: Gate Charge Characteristics

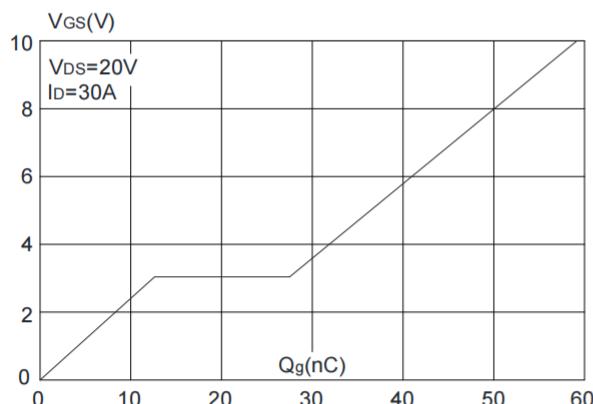


Figure 2: Typical Transfer Characteristics

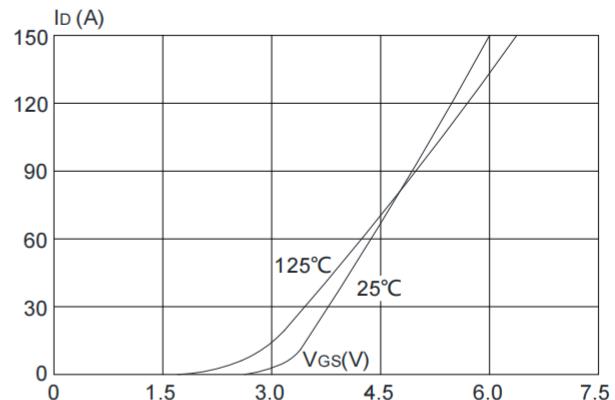


Figure 4: Body Diode Characteristics

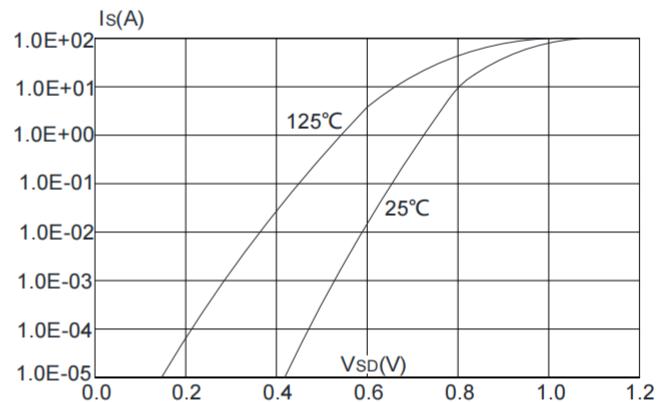


Figure 6: Capacitance Characteristics

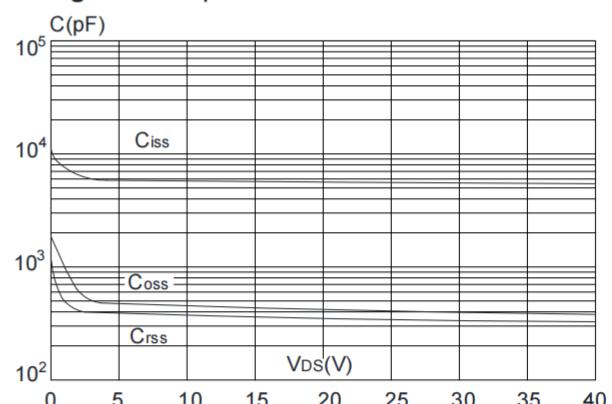


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

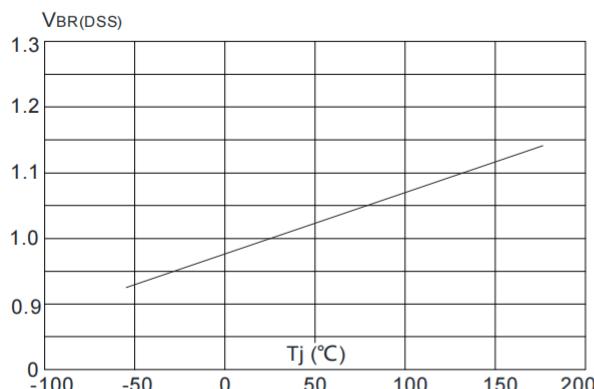


Figure 9: Maximum Safe Operating Area

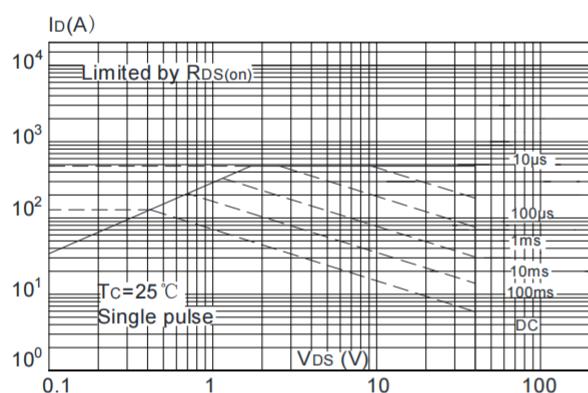


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

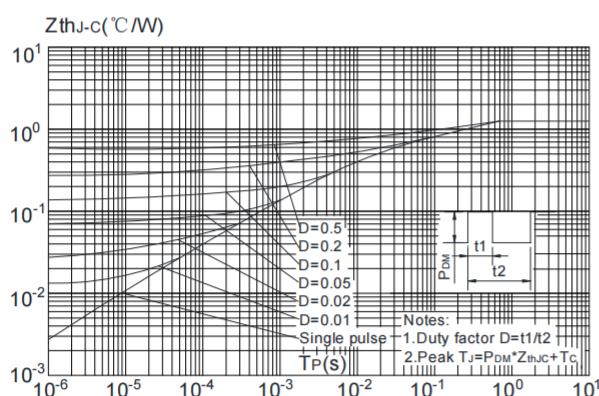


Figure 8: Normalized on Resistance vs. Junction Temperature

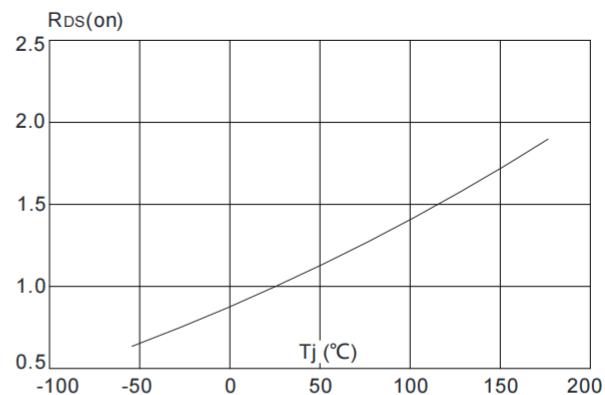
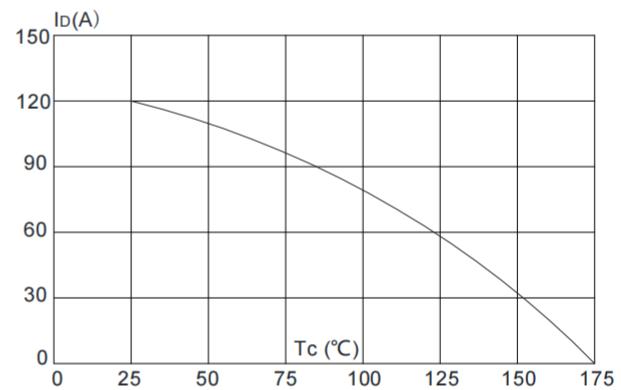


Figure 10: Maximum Continuous Drain Current vs. Case Temperature



Test Circuit

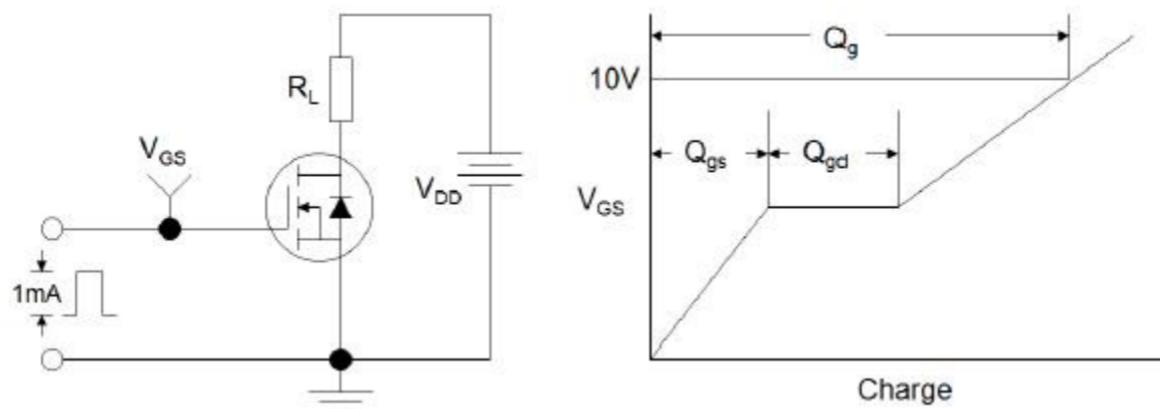


Figure 1: Gate Charge Test Circuit & Waveform

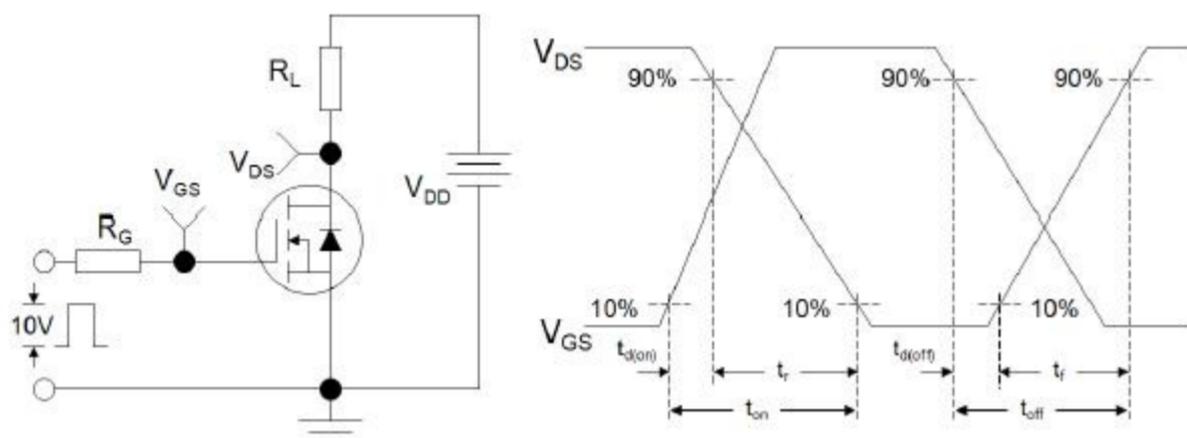


Figure 2: Resistive Switching Test Circuit & Waveforms

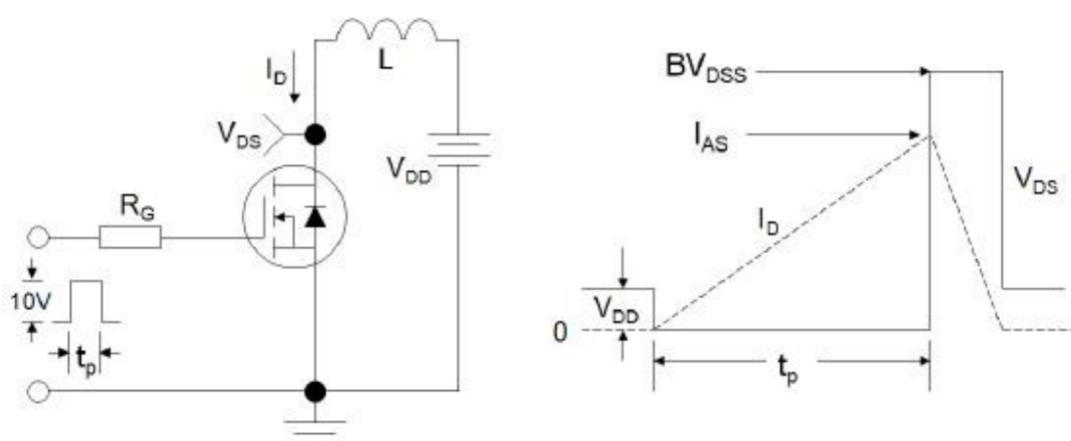


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

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