IR Receiver Modules for Remote Control Systems

FEATURES

- Very low supply current
- · Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage range: 2.5 V to 5.5 V
- · Improved immunity against modulated light sources HALOGEN
- · Insensitive to supply voltage ripple and noise
- Capable of side or top view
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

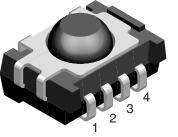
The TSOP36... series are miniaturized SMD IR receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a leadframe, the epoxy package contains an IR filter.

The demodulated output signal can be directly connected to a microprocessor for decoding.

The TSOP364.. series devices are optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. The AGC4 used in the TSOP364.. may suppress some data signals. The TSOP362.. series are provided primarily for compatibility with old AGC2 designs. New designs should prefer the TSOP364.. series containing the newer AGC4.

These components have not been qualified according to automotive specifications.

PARTS TABLE					
AGC		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	RECOMMENDED FOR LONG BURST CODES (AGC4)		
	30 kHz	TSOP36230	TSOP36430		
	33 kHz	TSOP36233	TSOP36433		
	36 kHz	TSOP36236	TSOP36436 (1)(2)(3)		
Carrier frequency	38 kHz	TSOP36238	TSOP36438 ⁽⁴⁾⁽⁵⁾		
	40 kHz	TSOP36240	TSOP36440		
	56 kHz	TSOP36256	TSOP36456 ⁽⁶⁾⁽⁷⁾		
Package		Panh	nead		
Pinning		1 = GND, 2 = N.C.	., 3 = V _S , 4 = OUT		
Dimensions (mm)		7.5 W x 5.3	3 H x 4.0 D		
Mounting		SM	1D		
Application		Remote	control		
Best remote control code (1) RC-5 (2) RC-6 (3) Pana		⁽¹⁾ RC-5 ⁽²⁾ RC-6 ⁽³⁾ Panasonic ⁽⁴⁾ NEC	c ⁽⁴⁾ NEC ⁽⁵⁾ Sharp ⁽⁶⁾ r-step ⁽⁷⁾ Thomson RCA		



MECHANICAL DATA

ORDERING CODE

TSOP36...TT - top view taped

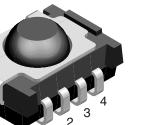
TSOP36...TR - side view taped

 $1 = GND, 2 = N.C., 3 = V_S, 4 = OUT$

Pinning:

Taping:

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16797





COMPLIANT

FREE

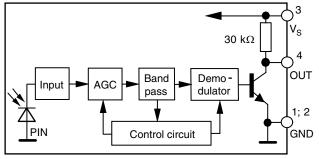
GREEN

(5-2008)

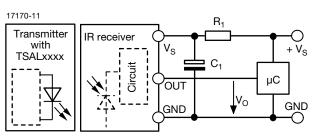




BLOCK DIAGRAM



APPLICATION CIRCUIT



 $R_{\rm 1}$ and $C_{\rm 1}$ recommended to reduce supply ripple for $V_{\rm S}$ < 2.8 V

16839

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage (pin 3)		Vs	-0.3 to +6	V
Supply current (pin 3)		I _S	3	mA
Output voltage (pin 4)		Vo	-0.3 to (V _S + 0.3)	V
Output current (pin 4)		lo	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPT	CAL CHARACTERISTICS	(T _{amb} = 25 °	°C, unless o	otherwise s	pecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply ourrept	$E_v = 0, V_S = 3.3 V$	I _{SD}	0.27	0.35	0.45	mA
Supply current	E _v = 40 klx, sunlight	I _{SH}	-	0.45	-	mA
Supply voltage		Vs	2.5	-	5.5	V
Transmission distance	$E_v = 0$, test signal see Fig. 1, IR diode TSAL6200, $I_F = 200 \text{ mA}$	d	-	45	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	$\begin{array}{l} \mbox{Pulse width tolerance:} \\ t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_o, \\ \mbox{test signal see Fig. 1} \end{array}$	E _{e min.}	-	0.12	0.25	mW/m ²
Maximum irradiance	t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see Fig. 1	E _{e max.}	30	-	-	W/m ²
Directivity	Angle of half transmission distance	φ1/2	-	± 50	-	deg

TSOP362.., TSOP364..





TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)

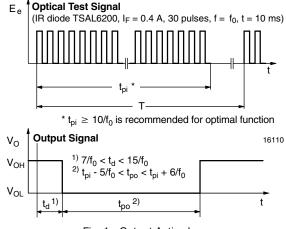


Fig. 1 - Output Active Low

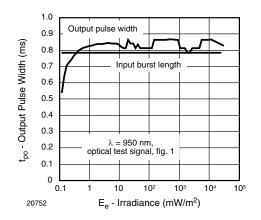
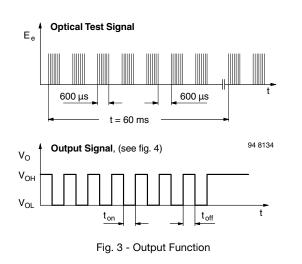


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient



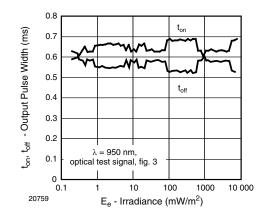


Fig. 4 - Output Pulse Diagram

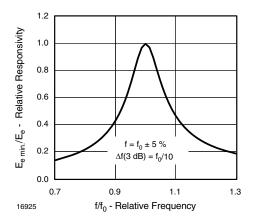


Fig. 5 - Frequency Dependence of Responsivity

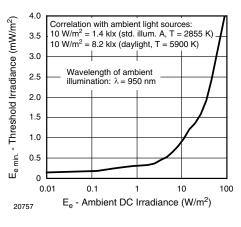


Fig. 6 - Sensitivity in Bright Ambient

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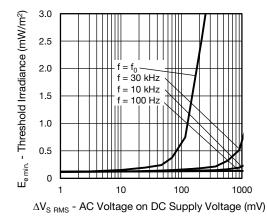


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

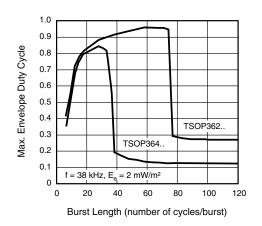


Fig. 8 - Maximum Envelope Duty Cycle vs. Burst Length

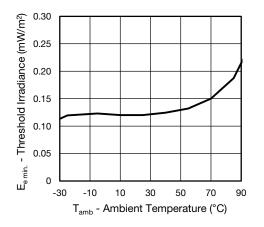


Fig. 9 - Sensitivity vs. Ambient Temperature

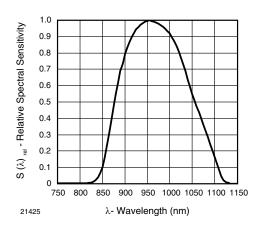


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

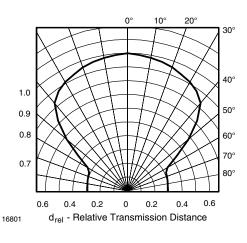


Fig. 11 - Horizontal Directivity

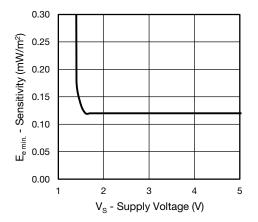


Fig. 12 - Sensitivity vs. Supply Voltage

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SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

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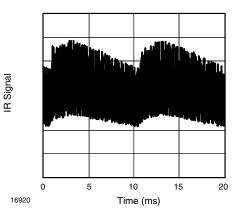


Fig. 13 - IR Disturbance from Fluorescent Lamp with Low Modulation

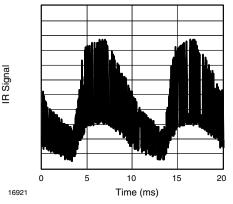


Fig. 14 - IR Disturbance from Fluorescent Lamp with High Modulation

	TSOP362	TSOP364
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 10 cycles	10 to 35 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	1800	1500
NEC code	Yes	Preferred
RC5/RC6 code	Yes	Preferred
Thomson 56 kHz code	Yes	Preferred
Sharp code	Yes	Preferred
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 13)	Complex and critical disturbance patterns are suppressed (example: signal pattern of Fig. 14 or highly dimmed LCDs)

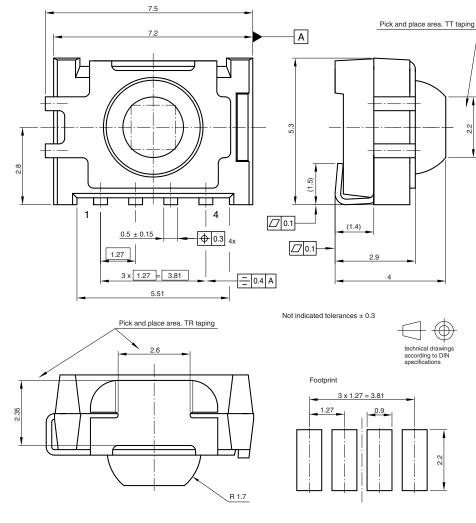
Notes

• For data formats with short bursts please see the datasheet for TSOP361.., TSOP363.., TSOP365...

• For Sony 12, 15, and 20 bit IR codes please see the datasheet of TSOP36S40F



PACKAGE DIMENSIONS in millimeters



Drawing-No.: 6.544-5341.01-4 Issue: 8; 02.09.09

ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

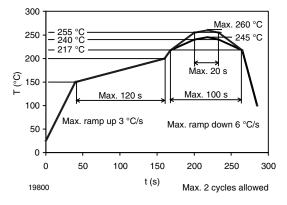
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- · Handle products only after the temperature has cooled off



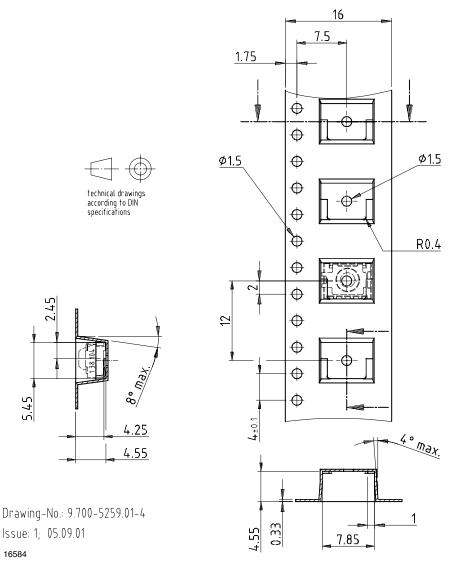
TSOP362.., TSOP364..

Vishay Semiconductors

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TT DIMENSIONS in millimeters

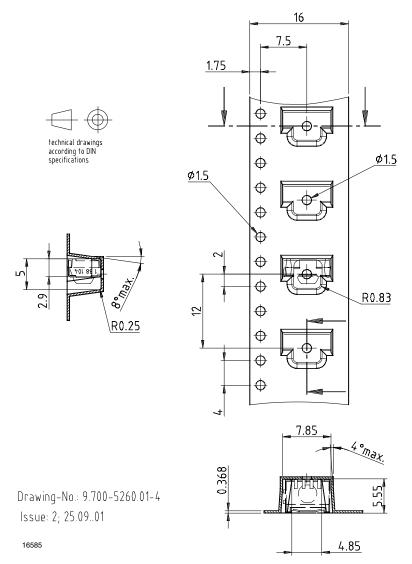


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TAPING VERSION TSOP..TR DIMENSIONS in millimeters



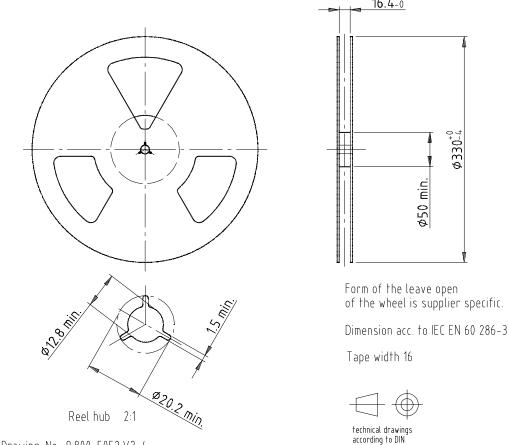


REEL DIMENSIONS in millimeters

330-

¢50 min.

16.4-0

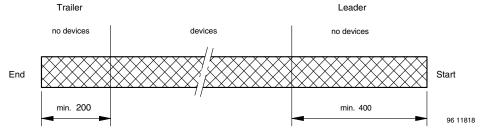


Drawing-No.: 9.800-5052.V2-4 Issue: 1; 07.05.02

technical drawings according to DIN specifications

16734

LEADER AND TRAILER DIMENSIONS in millimeters



COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 mm/min. ± 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

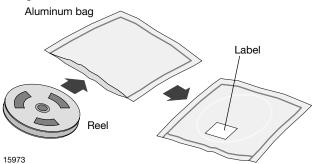
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.



VISHAY SEMICONDUCTOR Gmb	H STANDARD BAR CODE PROD	UCT LABEL (finished goods)
PLAIN WRITTING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxx+	Company logo
LONG BAR CODE TOP	ТҮРЕ	LENGTH
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
SHORT BAR CODE BOTTOM	ТҮРЕ	LENGTH
Selection-code	Х	3
Data-code	Ν	3
Batch-number	Х	10
Filter	-	1
Total length	-	17

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ / - 0 $^{\circ}\text{C}$ and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard J-STD-020 level 4 label is included on all dry bags.

(CAUTION This bag contains MOISTURE-SENSITIVE DEVICES
1.	Shelf life in sealed bag: 12 months at < 40 $^\circ\text{C}$ and < 90 $\%$ relative humidity (RH)
	After this bag is opened, devices that will be subjected to soldering reflow or equivalent processing (peak package body temp. 260 °C) must be
	Mounted within 72 hours at factory condition of < 30 $^\circ C/60$ % RH or Stored at < 5 % RH
3.	Devices require baking befor mounting if: Humidity Indicator Card is > 10 % when read at 23 $^\circ$ C ± 5 $^\circ$ C or 2a. or 2b. are not met.
4.	If baking is required, devices may be baked for: 192 hours at 40 °C + 5 °C/-0 °C and < 5 % RH (dry air/nitrogen) or 96 hours at 60 °C ± 5 °C and < 5 % RH for all device containers or 24 hours at 125 °C ± 5 °C not suitable for reels or tubes
Bag	Seal Date:
	(If blank, see barcode label)
No	te: Level and body temperature defined by EIA JEDEC Standard J-STD-020
122	

EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags

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ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





Tape and Reel Standards for SMD IR Receiver Modules

Vishay Semiconductor SMD IR receivers are packaged on tape and reel. The following specification is based on IEC publication 286, which takes the industrial requirements for automatic insertion into account.

Absolute maximum ratings, mechanical dimensions, optical and electrical characteristics for taped devices are identical to the basic catalog types and can be found in the specifications for untaped devices.

PACKAGING

The tapes of components are available on reels. Each reel is marked with labels which contain the following information:

- Vishay
- Туре
- Group
- Tape code, normally part of type name
- Production code
- Quantity

MISSING COMPONENTS

Up to 3 consecutive components may be missing if the gap is followed by at least 6 components. A maximum of 0.5 % of the components per reel quantity may be missing. At least 5 empty positions are present at the start and the end of the tape to enable tape insertion.

Tensile strength of the tape: > 15 N

NUMBER OF COMPONENTS

- A. Panhead SMD: quantity per reel: TT, SMD top view package, 1190 pcs
 - TR, SMD side view package, 1120 pcs
- B. Heimdall: quantity per reel: TT, Heimdall top view package, 2200 pcs
 - TR, Heimdall side view package, 2300 pcs
- C. Heimdall without lens: quantity per reel: WTT, top view package, 2200 pcs WTR, side view package, 2300 pcs
- D. Belobog: quantity per reel:
 - TT1, 1800 pcs
 - TT2, 7000 pcs
 - TR, not available in side view
- E. Belobog with shield: quantity per reel:
 - TT1, 1500 pcs TT2, 5000 pcs
- F. Minimold DF1P: quantity per reel: DF1P, 1100 pcs

ORDER DESIGNATION

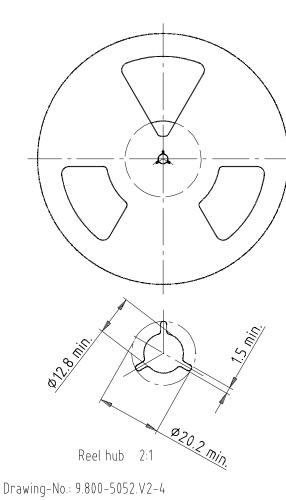
The type designation of the device is extended by TT or TT1 for top view or TR for side view.

Example:

TSOP6238TR (reel packing) TSOP75238TR (reel packing) TSOP75338WTT (reel packing) TSOP57438TT1 (reel packing) TSOP57238HTT1 (reel packing)



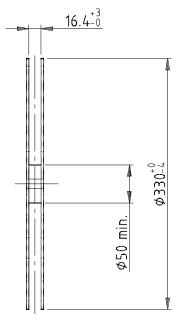
REEL DIMENSIONS FOR PANHEAD SMD AND HEIMDALL in millimeters



Drawing-No.: 9.800-5052.V Issue: 1; 07.05.02 16734

Note

• The body structure of the reel can vary



Form of the leave open of the wheel is supplier specific.

Dimension acc. to IEC EN 60 286-3

Tape width 16



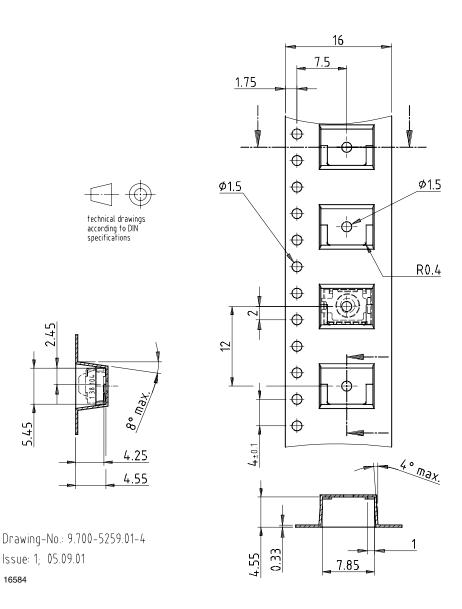
technical drawings according to DIN specifications

Rev. 2.2, 01-Jul-16



TAPING VERSION TSOP..TT (TOP VIEW) DIMENSIONS in millimeters

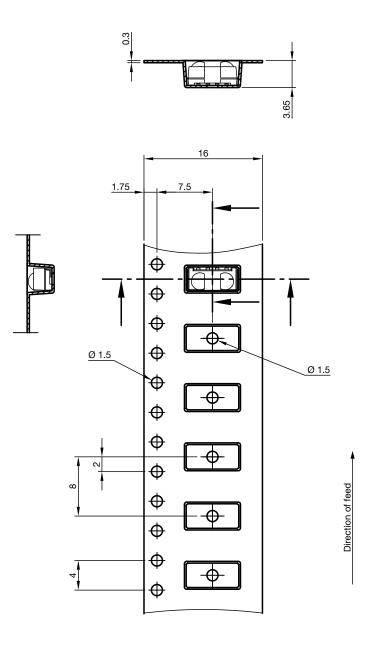
A. Panhead SMD (TSOP36...TT, TSSP....TT, TSOP6...TT)





TAPING VERSION TSOP..TT (TOP VIEW) DIMENSIONS in millimeters

B. Heimdall SMD (TSOP75...TT, TSOP77...TT, TSSP77...TT)





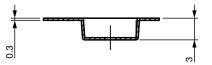
technical drawings according to DIN specifications

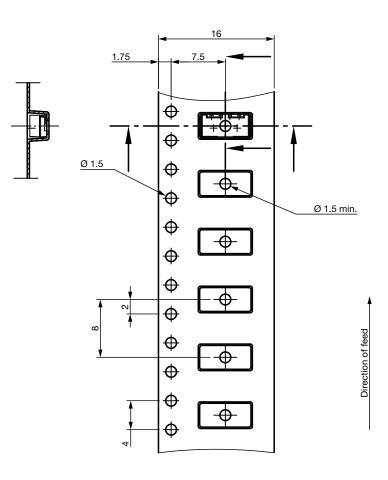
Drawing-No.: 9.700-5338.01-4 Issue: 4; 12.06.13



TAPING VERSION TSOP..TT (TOP VIEW) DIMENSIONS in millimeters

C. Heimdall SMD without lens (TSOP75...WTT, TSOP77...WTT, TSSP77...WTT)







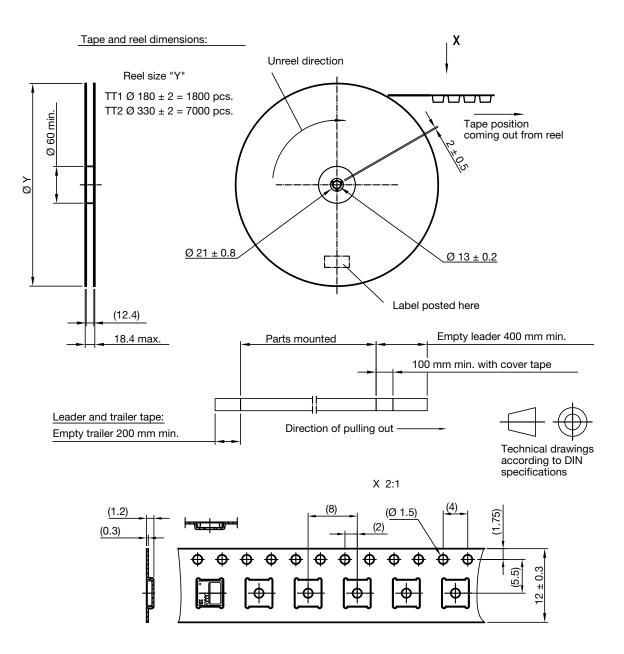
technical drawings according to DIN specifications

Drawing-No.: 9.700-5341.01-4 Issue: 3; 06.10.15



TAPING VERSION TSOP..TT1, TSOP..TT2 (TOP VIEW) DIMENSIONS in millimeters

D. Belobog (TSOP37...TT1, TSOP37...TT2, TSOP57...TT1, TSOP57...TT2)



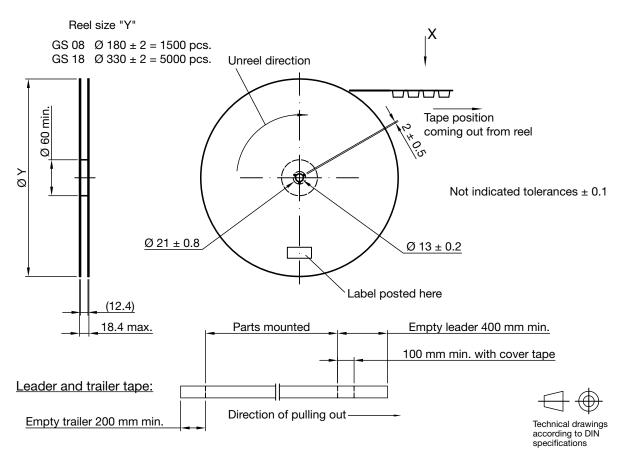
Drawing-No.: 9.700-5347.01-4 Issue: 1; 14.11.11 Not indicated tolerances ± 0.1



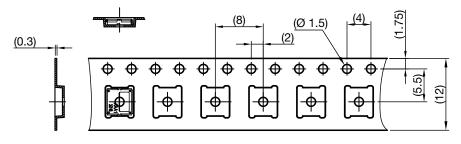
TAPING VERSION TSOP..TT1, TSOP..TT2 (TOP VIEW) DIMENSIONS in millimeters

E. Belobog with shield (TSOP37...HTT1, TSOP37...HTT2, TSOP57...HTT1, TSOP57...HTT2)

Tape and Reel dimensions:



X 2:1



Reel dimensions and tape

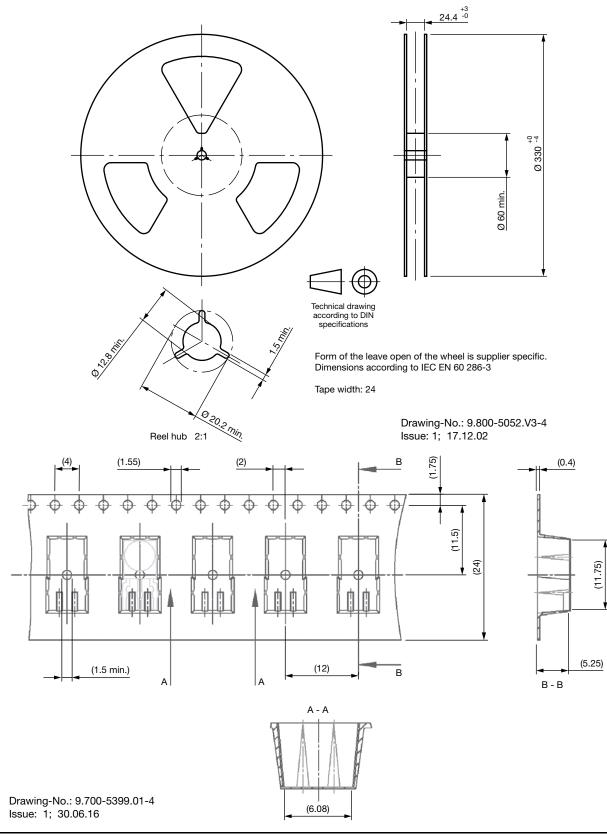
Drawing-No.: 9.700-5380.01-4 Issue: 1; 28.10.13

TAPING VERSION TSOP..DF1P (SIDE VIEW) DIMENSIONS in millimeters

F. Minimold DF1P (TSOP33...DF1P, TSOP53...DF1P)

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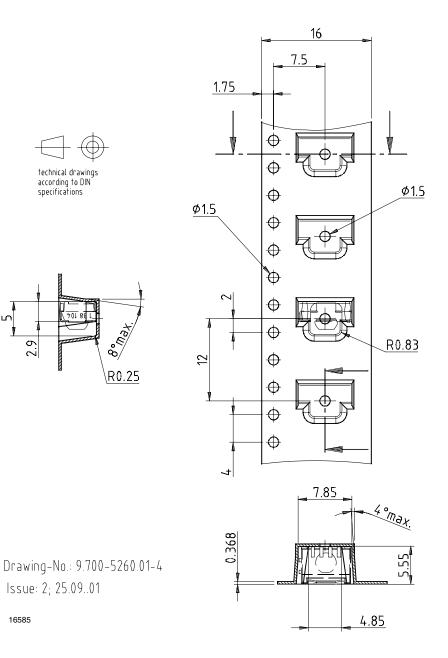
Rev. 2.2, 01-Jul-16

8



TAPING VERSION TSOP..TR (SIDE VIEW) DIMENSIONS in millimeters

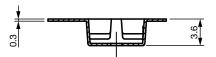
A. Panhead SMD (TSOP36...TR, TSSP6...TR, TSOP6...TR)

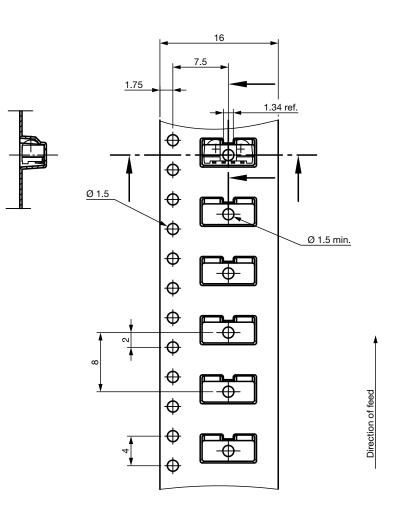




TAPING VERSION TSOP..TR (SIDE VIEW) DIMENSIONS in millimeters

B. Heimdall SMD (TSOP75..., TSOP77..., TSSP7....)







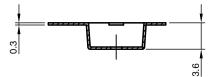
technical drawings according to DIN specifications

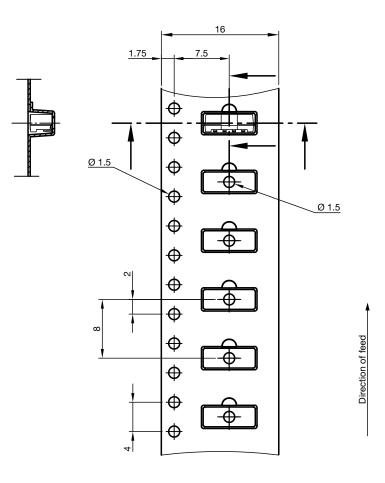
Drawing-No.: 9.700-5337.01-4 Issue: 2; 06.10.15



TAPING VERSION TSOP..TR (SIDE VIEW) DIMENSIONS in millimeters

C. Heimdall SMD without lens (TSOP75...WTR, TSOP77...WTR, TSSP...WTR)





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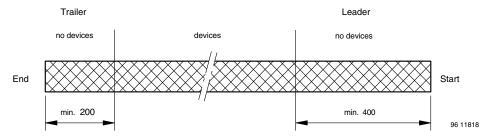
technical drawings according to DIN specifications

Drawing-No.: 9.700-5342.01-4 Issue: 2; 12.06.13





LEADER AND TRAILER DIMENSIONS in millimeters



COVER TAPE REEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 mm/min. \pm 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

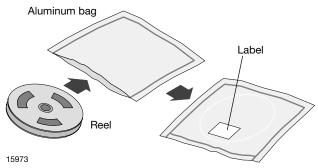
PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	XXXXXXX+	Company logo
LONG BAR CODE TOP	ТҮРЕ	LENGTH
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
SHORT BAR CODE TOP	ТҮРЕ	LENGTH
Selection-code	Х	3
Data-code	Ν	3
Batch-number	Х	10
Filter	-	1
Total length	-	17

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DRY PACKAGING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 $^\circ\text{C}$ + 5 $^\circ\text{C}$ and < 5 % RH for all device containers or

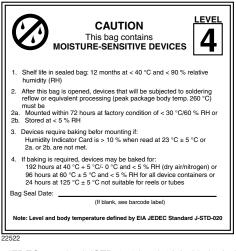
24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard JSTD-020 level 4 label is included on all dry bags.

OUTER PACKAGING

The sealed reel is packed into a pizza box.

Vishay Semiconductors



EIA JEDEC standard JSTD-020 level 4 label is included on all dry bags

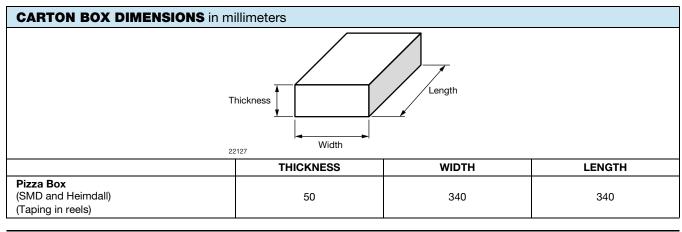
ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





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