

## Single channel low-side gate driver

Data brief



### Features

- Low-side MOSFET driver
- 1 A sink and 0.8 A source capability
- External reference for input threshold
- Wide supply voltage range (10 V ÷ 18 V)
- Input and output pull-down resistors
- Short propagation delays
- Input and output UVLO
- Wide operating temperature range: -40 °C to 125 °C
- SOT23-5 package

### Applications

- SMPS
- Digital lighting
- Wireless battery chargers
- Digitally controlled MOSFETs

### Description

The PM8841 is a high frequency single channel low-side MOSFET driver specifically designed to work with digital power conversion microcontrollers, such as the STMicroelectronics STLUX™ family of products.

The PM8841 output can sink 1 A and source 0.8 A.

The input levels of the driver are derived by the voltage present at the IN\_TH pin (between 2 V and 5.5 V). This pin is typically connected at the same voltage of the microcontroller supply voltage.

The PM8841 device includes both input and output pull-down resistors.

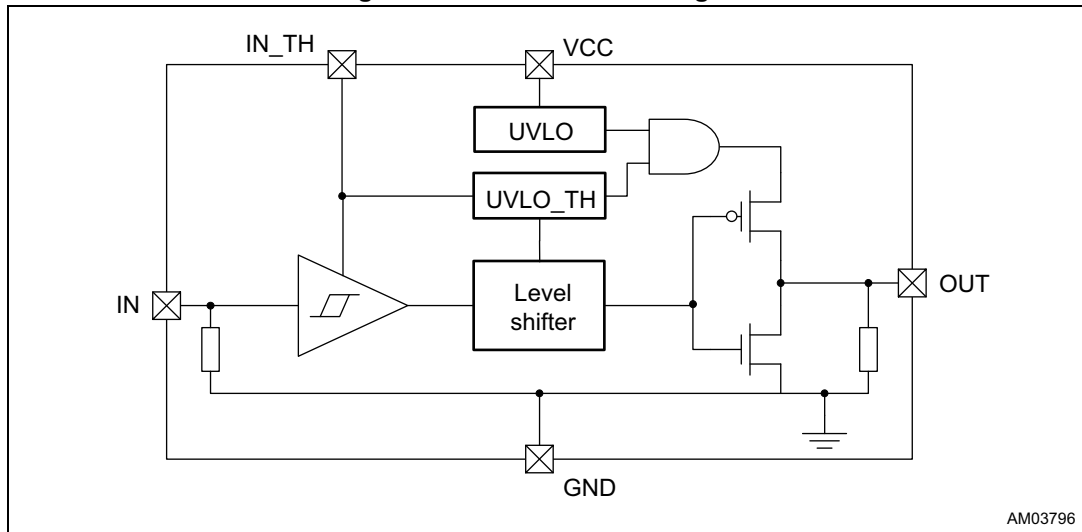
UVLO circuitry for input and output stages is present preventing the IC from driving the external MOSFET in unsafe condition.

**Table 1. Device summary**

Order code	Package
PM8841D	SOT23-5

# 1 Block diagram

Figure 1. PM8841D block diagram



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## 2 Pin connection

Figure 2. Pin connection

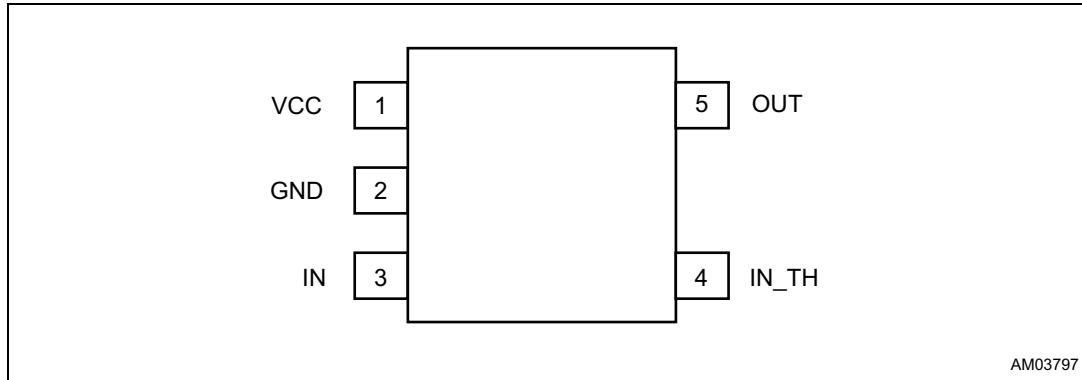


Table 2. Pin description

Symbol	Pin	Description
VCC	1	IC power supply. A voltage comprised between 10 V and 18 V can be connected between this pin and GND to supply the IC.
GND	2	Reference voltage connection.
IN	3	Digital input signal for driver. It is internally pulled down to GND with a 100 k $\Omega$ (typ.) equivalent resistor.
IN_TH	4	Input for the IN pin's threshold definition: a voltage can be applied obtaining the values for VIH and VIL.
OUT	5	MOSFET gate drive sourcing / sinking output controlled by the IN pin. A pull-down equivalent resistor [50 k $\Omega$ (typ.)] is present.

### 3 Typical applications

Figure 3. Test circuit

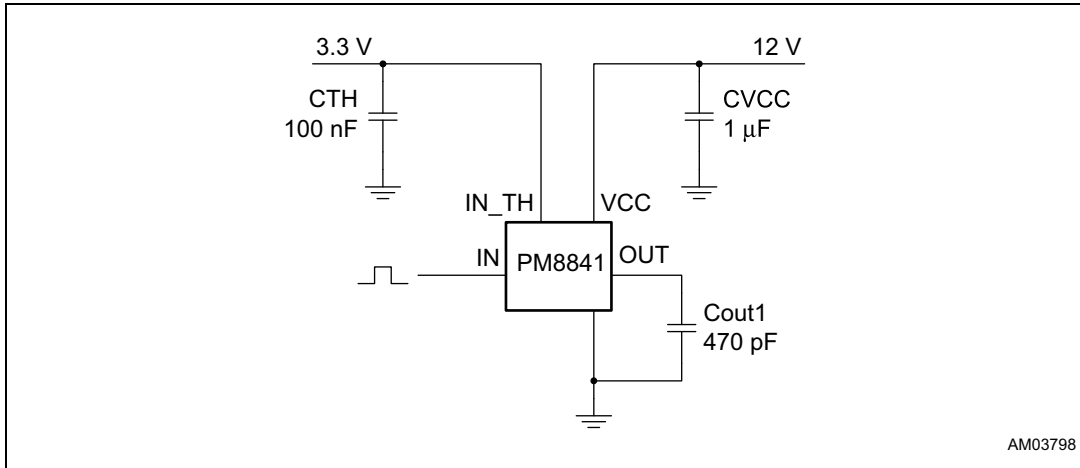


Figure 4. Digitally controlled PFC boost converter

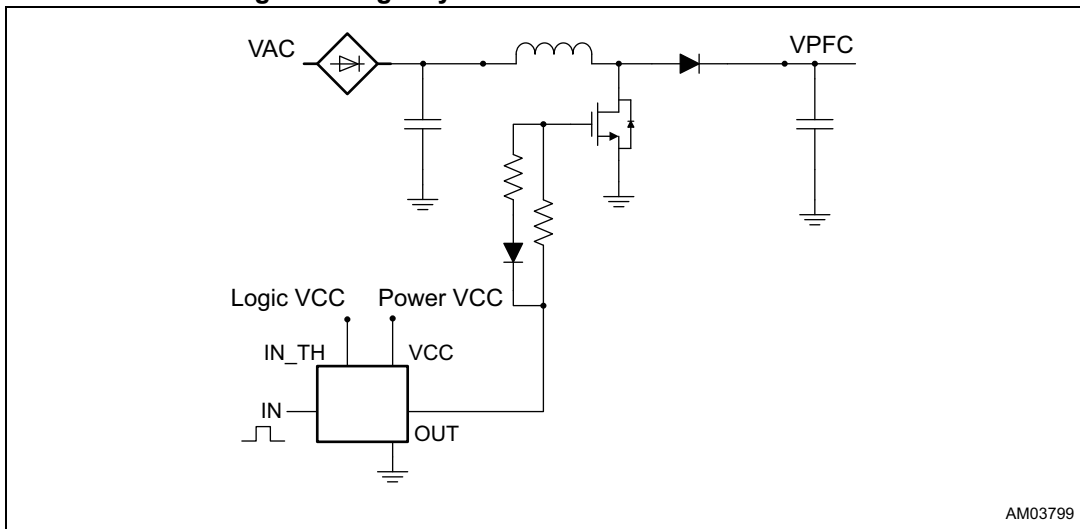
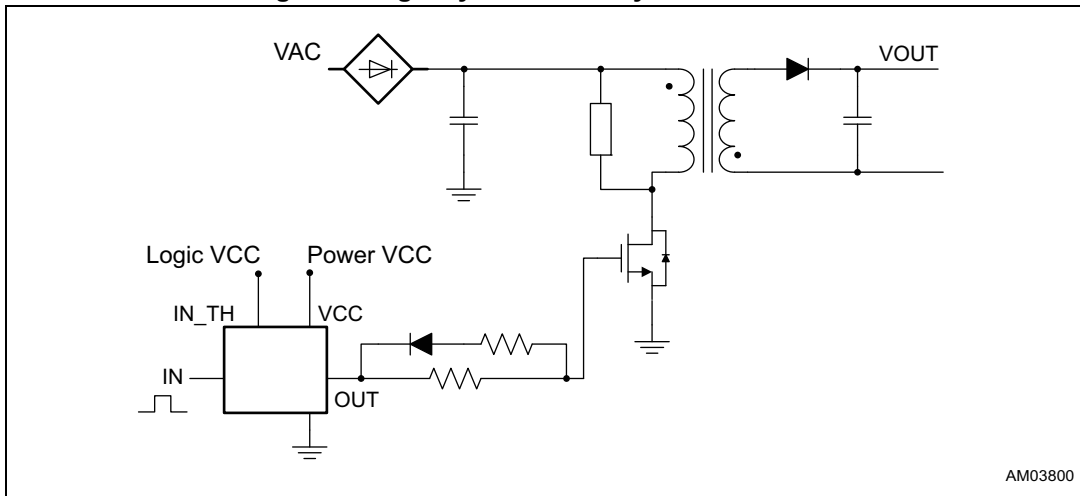
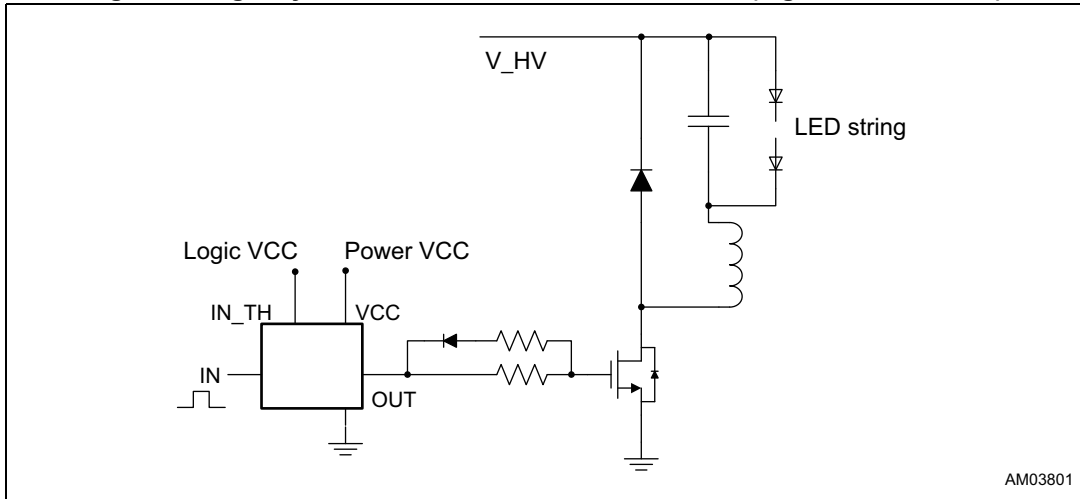


Figure 5. Digitally controlled flyback converter



AM03800

Figure 6. Digitally controlled inverse buck converter (e.g. LED controller)



AM03801

## 4 Revision history

Table 3. Document revision history

Date	Revision	Changes
13-Jun-2014	1	Initial release.

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