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SCM1201A Transformer Driver for Isolated Power Supplies

Features

- Push-Pull Driver for Small Transformers
- 4..5~8V Wide input voltage
- · Built in soft start function,
- Two built-in power MOSFET and Highly symmetrical drive
- Output short circuit protection
- Over temperature protection

Application

- DC-DC Isolation Converter
- Isolated Interface Power Supply for CAN, RS-485 RS-422, RS-232, SPI, I2C, Low-Power LAN.
- Industrial Automation
- Process Control
- Medical Equipment

Packaging



Optional Packaging of Product: SOT23-6, please refer to "Order Information" for details of silk screen.

Description

The SCM1201A is a oscillator/power-driver integrated with MOSFET, specifically designed for small form factor, isolated .power supplies in isolated interface applications. The SCM1201A drives a low-profile, center-tapped transformer primary from a 4..5~8V wide DC power supply. The secondary can be wound to provide any isolated voltage based on transformer turns ratio when V_{VD1}/V_{VD2} is less than absolute maximum rating.

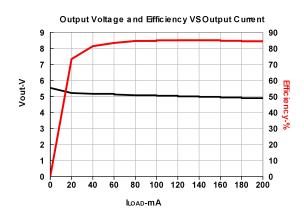
The driver of internal MOSFETs is highly symmetrical, thereby decreasing the degree of magnetic biasing of push-pull topology. The chip also is integrated with three key technologies to improve reliability:soft start, output short circuit protection, and over temperature protection.

The SCM1201A is available in a small SOT23-6 package, and is specified for operation at temperatures from --40℃ to 125℃.

Simplified Schemetic

RB160M-60 + 0.47uF1 vss vD2 6 - 1uF + Vout=5V RB160M-60 RB160M-60

Function Curves



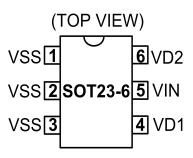
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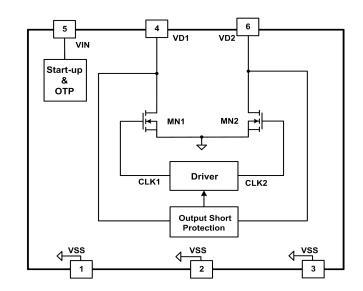
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Pin Configuration



Inter Block Diagram



Pin Description

Pin Number	Name	I/O	Description
1	VSS	I	Device ground. Connect this pin to board ground. In application, it is better to connect the pin 1 and pin 3
2	VSS	I	together with the device with good heat dissipation performance, so that the internal heat of chip can be
3	VSS	I	quickly conducted out.
4	VD1	I	Open Drain output 1. Connect this pin to one end of the transformer primary side
5	VIN	Р	Supply voltage input. Connect this pin to the center-tap of the transformer primary side. Buffer this voltage with a 1 μ F to 10 μ F ceramic capacitor
6	VD2	I	Open Drain output 2. Connect this pin to the other end of the transformer primary side.

Absolute Maximum Ratings

General test conditions: Free-air, normal operating temperature range (unless otherwise specified).

Paramete	Min	Max	Unit	
Input Voltage	V _{VIN}	-0.4	10	V
Drain Voltage of MOSFET	V_{VD1}/V_{VD2}	-0.7	27	V
Drain Peak Current of MOSFET	I_{D1P}, I_{D2P}		mA	
Operation Junction Temperature Range	TJ	-40 150		
Storage Temperature	T _{STG}	-55	150	_ ℃
Temperature of Lead with the distance of 0.6mm from the		260		
shell within 10 seconds			200	
Moisture Sensitivity Level	MSL	MSL3		
Rated Value of ESD	HBM		2000	\/
Nated value of ESD	CDM		1000] '
Continuous Power Dissipation	Ртот		270	mW

Note: if the value exceeds the stress value listed in the table's "maximum value", it may cause permanent damage to the components. If the product operates in the maximum rated condition for a long time, the reliability of the components may be affected. All voltage values take GND as basis reference. The current refers to the current between positive input and negative output of the specified terminal.

Recommended Operating Conditions

Unless otherwise specified, the following parameters are measured in the conditions of V_{VIN} = 5V.

Parameters		Min	Max	Unit
Input Voltage	V_{VIN}	4	8	V
Drain Voltage of MOSFET	V_{VD1}/V_{VD2}	-0.7	20	V
Output Switching Current of Primary Winding	I _{D1} , I _{D2}		350	mA
Operation Junction Temperature	TJ	-40	125	$^{\circ}$

Thermal Resistance Information

Heating current is 1A and heating time is 300s; test current is 10mA and test time is 300s.

Parameter		Value	Unit
Junction-to-ambient thermal resistance	θ_{JA}	196	°C/W

Note: Because SOT23-6 has small packaging size, thermal resistance between components and board θ_{JB} and thermal resistance between components and shell θ_{JC} have no reference value for system design. The reference standard is JESD51-1.

Electrical Characteristics

Unless otherwise specified, V_{VIN} = 5V and the environment temperature is 25°C.

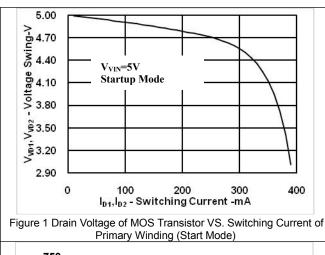
Symbol	Corresponding Parameters	Test Conditions	Min	Тур	Max	Unit
Supply Section (VIN	Pin)					
Irun	Operating Current of Chip	V _{VIN} =5V	0.4	0.8	1.2	mA
ISTART	I _{VIN} when V _{VIN} is in under-voltage lockout	V _{VIN} =3V		1.26		mA
V _{VIN_ON}	Start-up Voltage	V _{VIN} voltage increasing	3.4	3.7	4	V
V_{VIN_OFF}	Voltage when V _{IN} is in under-voltage lockout	V _{VIN} voltage decreasing	2.7	3	3.3	V
T_{OTP}	Temperature of Over-temperature Protection			162		$^{\circ}$
Тотрн	Return Difference of Over-temperature Protection			32		℃
Drain Port of MOSFE	ET (VD1/VD2 Pin)				•	
B _{VDSS}	Breakdown Voltage of MOS Transistor	V_{VIN} =0 V , I_{DS} =100 u A	27	43		V
Б.	On Presintance (V. 5)()	T _J =25℃,I _{DS} =0.2A		0.38	1.5	
R _{DS_ON}	On Resistance (V _{VIN} =5V)	T _J =100℃,I _{DS} =0.2A		0.47		1
Б	On Projeton on AVA	T _J =25℃, I _{DS} =0.2A		0.43		Ω
R _{DS_ON}	On Resistance (V _{VIN} =4V)	T _J =100℃,I _{DS} =0.2A		0.53		1
I _{SOFT}	Current of Soft Start	$V_{VD1}=V_{VD2}=3V$	350	475	600	mA
Internal Time						
Fosc	Operating Frequency		246	273	300	kHz
T _{D_OSP}	Delay Time of Short Circuit Protection	Fosc=250kHz		100		ms
T _{SLEEP}	Sleep Time of Short Circuit Protection	F _{OSC} =250kHz		800		ms

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Symbol	Corresponding Parameters	Test Conditions	Min	Тур	Max	Unit
t _r	VD1, VD2 output rise time	V _{VIN} =5V, See Figure 9		20		ns
t _f	VD1, VD2 output fall time	V _{VIN} =5V, See Figure 9		10		ns
t _{BBM}	Break-before-make time	V _{VIN} =5V, See Figure 9		100		ns

Typical Performance Curves

Unless otherwise specified, the following typical characteristic curves are obtained in the conditions of Vvin=5V and T=25°C. TP1 curve is obtained by testing the test circuit shown in Figure 7.



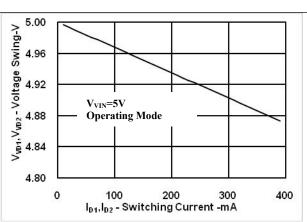


Figure 2 Drain Voltage of MOS Transistor VS. Switching Current of Primary Winding (Steady State Mode)

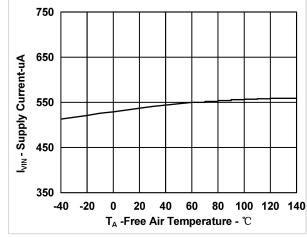


Figure 3 Average Supply Current of VIN VS Free Air Temperature

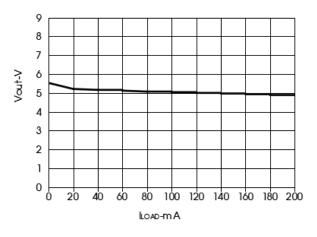
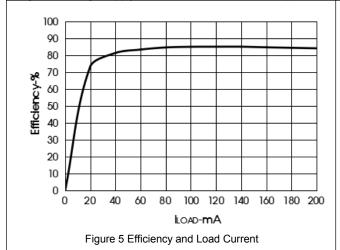
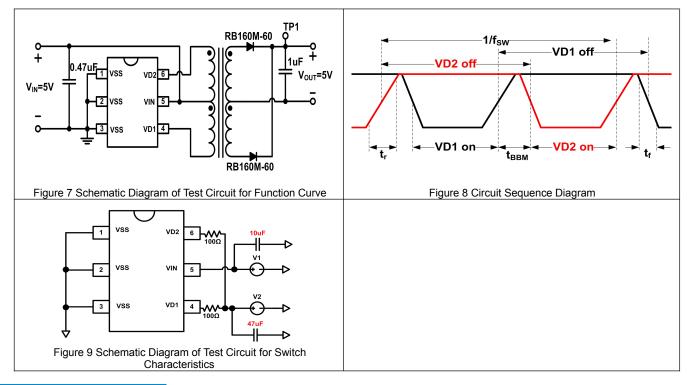


Figure 4 Relationship between Output Voltage and Load Current;



245 240 requency-kHz 235 230 225 220 -40 -20 0 20 40 60 80 100 120 T_A -Free Air Temperature - °C

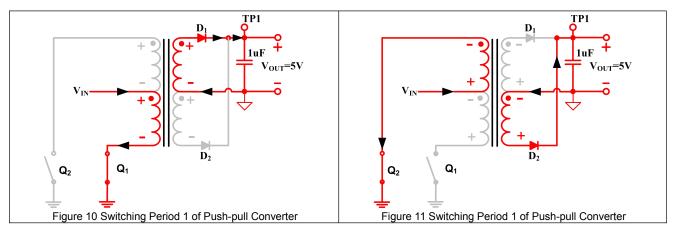
Figure 6 Switching Frequency of MOS Transistor VS Free Air Temperature



Features Description

(1) Push-pull Converter

As shown in Figure 10 and Figure 11, the push-pull converter is a transformer with center tap, which can achieve the transmission of energy from the primary winding to secondary winding.



The drive waveform of drains VD1 and VD2 of two MOSFETs Q1 and Q2 are shown in Figure 8. Two MOS transistors are on alternatively and the times of the breakover periods of two transistors are equal, and there is a short period t_{BBM} between the two breakover periods that the two power transistors are not on. That is to say, the drive levels of two MOS transistors are quasi complementary in time sequence, that is the other transistor is off when one transistor is on, but there is a short period of dead time during the switch to prevent the backward current flow when two the transistors are not on simultaneously. As shown in red highlighted parts in Figure 10, when Q1 is on, input voltage V_{IN} drives a current which arrives at the reference ground through the lower half of primary winding of transformer and Q1, and at the same time, the induced electromotive force of side winding charges output capacitor through diode D1; similarly, as shown in Figure 11, when Q2 is on, the induced electromotive force charges output capacitor through diode D2. As continuously repeating the above process, the secondary winding of power converter obtain the needed power supply.

(2) Magnetization of Magnetic Core

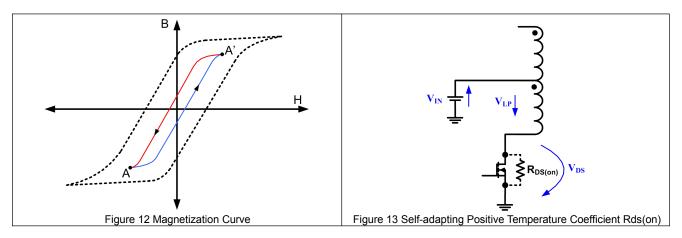
Figure 12 is the ideal magnetization curve of push-pull converter, and the vertical axis represents magnetic flux density B and the horizontal axis represents magnetic field intensity H. When Q1 is on, the magnetic flow is pushed to point A' from point A; similarly, when Q2 is on, the magnetic flow is then pulled back to point A from point A'. The magnetic flux density B is proportional to the product of voltage of primary winding V_{LP} and breakover time of MOS transistors t_{ON} , which can be described in the following formula:

$$B{\approx}V_{Lp}{\times}t_{ON}$$

The volt-second product $V_{Lp} \times t_{ON}$ defines the magnetization degree of each switching period. If the volt-second products in the above "push" and "pull" periods are not identical, a small direct current component may be generated to cause the deviation of magnetic flow. If balance cannot be restored, the deviation of magnetic flow will gradually increase in the each of the following switching period, making magnetic core become saturated gradually. The phenomenon of the deviation of magnetic flow is usually caused by the unequal on resistance or switching speed of two power switching components. Although the on resistance or switching speed of the two power components are approximately equal through integrating them into the

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same wafer with the help of high matching advantage of semiconductor integrated circuit technology, the manufacturing error still exists, causing the small deviation for the breakover time.



Fortunately, the on resistance $R_{DS(on)}$ of MOSFET has positive temperature coefficient, with which SCM1201A has the self-correcting effect to restrain the imbalance of volt-second. Under the condition that there is small deviation between the breakover time of two MOS transistors, the transistor which has longer breakover time t_{ON} generates more quantity of heat, and the temperature of the transistor rises to improve $R_{DS(on)}$, then in the breakover period when the load remains unchanged, the drain-source voltage of this transistor V_{DS} is relatively high; as shown in Figure 13, the voltage of primary winding V_{LP} conforms to the formula V_{LP} = V_{IN} - V_{DS} , thus the V_{LP} of the transistor which has larger t_{ON} will gradually decrease to make volt-second recover balance.

Product Operation Mode

SCM1201A has three operation modes, which respectively are start-up mode, operation mode and short mode.

In start-up mode, SCM1201A provides sufficient charging time for output capacitor, to avoid the abnormal start caused by output short circuit which is incorrectly identified due to the excessive low voltage of output capacitor when it is just started; at the same time, the MOS transistor in start-up mode is always operating in current-limiting drive status which means that the drive voltage of MOS transistor in start is limited, thereby making the current flowing through MOS transistor restrained within the safe range of components, that is to restrain the output switching current of primary winding to I_{SOFT} to achieve the soft start of the system, thereby avoiding the over-current impact and the generation of excessive heat.

In operation mode, MOS transistor is always in full drive status which means that the MOS transistor is operating in switching status and the breakover voltage is very low, which guarantee the efficiency of converter.

In short mode, it will stop driving the converter in sleep mode and the heat generated in start-up mode will be dissipated, then the product changes to start-up mode.

The three operation modes can be freely switched. Only when there is abnormality of output short circuit, the product will repeatedly switch between the start-up mode and short mode; when the abnormality disappears, the product will automatically change to operation mode, all of which can fully guarantee the reliability of converter and have no influence on the performance of converter in normal operation.

Start-up Mode

The voltage of output capacitor is zero when the converter is just started, and the converter is firstly in start-up mode. The flow diagram is shown in Figure 14, that is, start \rightarrow Drive the selected MOS transistor in current-limiting drive method \rightarrow check the switch-on voltages of MOS transistors(V_{VD1} , V_{VD2}) \rightarrow judge whether the voltages (V_{VD1} , V_{VD2}) are more than the set value.

If (V_{VD1}, V_{VD2}) are more than the set value, then calculate the duration of over-voltage \rightarrow judge whether the duration is more than T_{D_OSP} (100ms,typ,). If yes, then the system turns into short circuit mode; if no, then the above process is repeated.

If (V_{VD1}, V_{VD2}) are not more than the set value, then the system turns into operation mode.

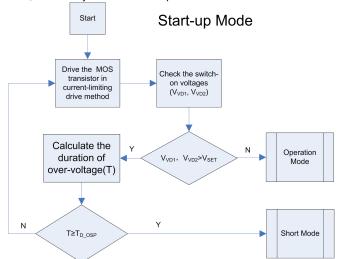


Figure 14 Flow Diagram of Start-up Mode

Operation Mode

If the output of converter has no short circuit, the voltage of output capacitor, in start-up mode, will gradually increase during the continuously circular charging. When the breakover voltage of MOS transistor is less than or equal to the set value, the converter will turn into operation mode. The flow diagram is shown in Figure 15, that is, determine that the breakover voltage of MOS transistor is less than or equal to the set value— drive the selected MOS transistor with full drive— check the switch-on voltage of MOS transistors— judge whether the voltages (V_{VD1}, V_{VD2}) are more than the set value.

If the breakover voltage of MOS transistor is more than the set value, then the system goes into time-counting cycle of start-up mode; otherwise, the system turns back to the step "drive the selected MOS transistor with full drive", and the above processes are repeated, which is the normal operation of the converter after the product is started. In the operation, MOS transistor is fully driven, that is the MOS transistor is operating in switching status and the on resistance is low, resulting in low energy consumption and high efficiency.

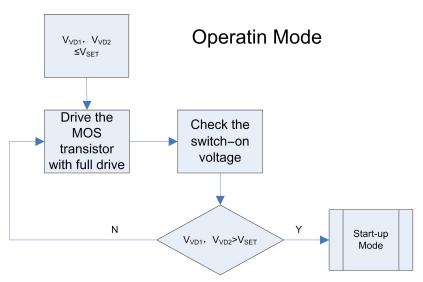


Figure 15 Flow Diagram of Operation Mode

Short Mode

If the output of the converter has short circuit, it will detect in start-up mode that the breakover voltage of MOS transistor is more than the set value, then the accumulated over-voltage time will definitely exceed $T_{D_{COSP}}$ (100ms,typ.). At this time, SCM1201A will stop to drive the MOS transistor and begin to count the time of stopping driving MOS transistor. When the time is counted to T_{SLEEP} (800ms,typ.), the product resumes operation and turns into start-up mode.

The flow diagram of short mode is shown as Figure 16: determine the duration of over-voltage exceeding $T_{D_OSP} \rightarrow$ stop driving and begin to count time (sleep mode) \rightarrow finish counting time \rightarrow turn back to start-up mode. We can see that if the converter is always in output short circuit status, it will operate in the short mode and start-up mode alternately.

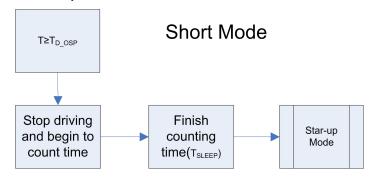
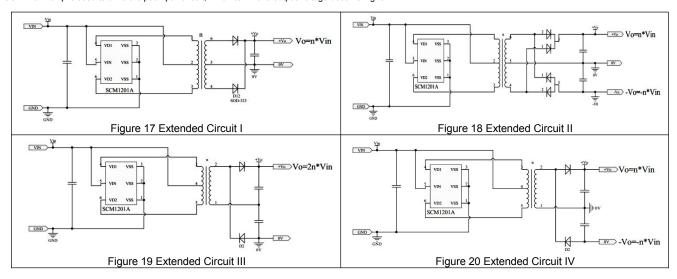


Figure 16 Flow Diagram of Short Mode

SCM1201A chip is used to drive the push-pull circuit, which can make output voltage become higher.



Application Circuit

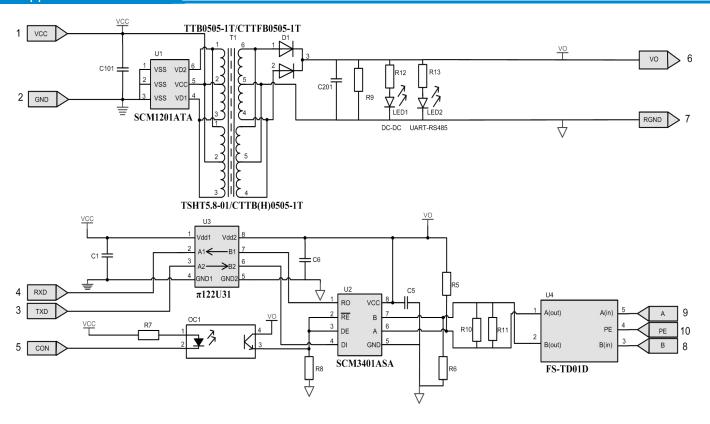


Figure 21 Application Circuit I

(1) Introduction of TTB05xx-1T Transformer

With the voltage of primary winding and secondary winding of 1650VDC, the allowable working temperature of -40°C ~+125°C and the packaging size of 6.50 x 8.80 x 3.60mm, combined with design of our ICSCM1201ATA product, TTB05xx-1T transformer can be used for electrical isolation scenario which is applicable to 5VDC input and output power less than 1W, such as digital circuit, analog acquisition circuit and data exchange circuit. Please log in the official website of Mornsun and contact the salespeople to obtain the specific specification.

(2) Introduction of TSHT5.8-01 Transformer TSHT5.8-01 transformer, with the packaging size of $12.50 \times 8.70 \times 5.90$ mm, is specially designed for use with IC. It is mainly used for the electrical isolation scenario which is applicable to 5VDC input and 5VDC output power less than 1W, such as digital circuit, analog acquisition circuit and data exchange circuit.

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If the input power is not stable enough, it is suggested to add 1uF capacitor in the first section of IC SCM1201A; if there is high requirement to EMI performance, add capacitor and inductor in the first section of the module to filter noise; if there is high requirement to no-load voltage, add resistor after the filtering capacitor of the module as dummy load; it is suggested that the connecting wire of IC4 and 6 pins to the transformer is as short as possible.

Ordering information

Product Model	Packaging	Quantity of Pin	Silk Screen	Packing
SCM1201ATA	SOT23-6	6	1201YM	3K/tray

Description of Product Model

SCM1201XYZ:

(1) SCM1201, product code.(2) X = A-Z, version code.

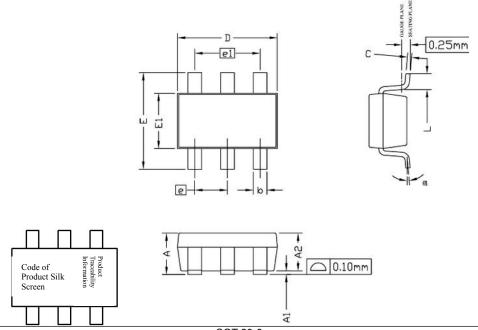
(4) Z = C, I, A, M, code of temperature range; C: 0°C-70°C, I: -40°C-85°C, A: -40°C-125°C, M: -55°C-125°C.

Description of Silk Screen 1201YM:

(1) 1201, code of product silk screen in 4 digits.

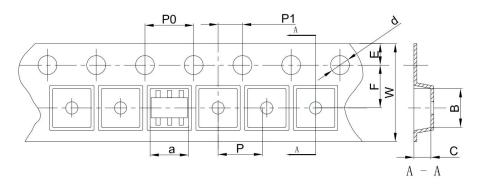
(2) YM, Product Traceability Code.

Package Information

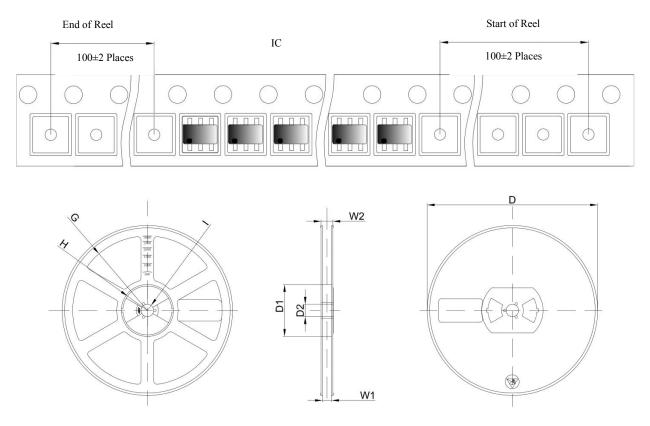


		SOT-23-6			
Mark	Dimens	ion (mm)	Dimension (inch)		
IVIAIK	Minimum Value	Maximum Value	Minimum Value	Maximum Value	
A	0.900	1.250	0.035	0.049	
A1	0.000	0.150	0.000	0.006	
A2	0.700	1.200	0.028	0.047	
b	0.300	0.500	0.012	0.020	
С	0.080	0.200	0.003	0.008	
D	2.700	3.100	0.106	0.122	
E	2.500	3.100	0.098	0.122	
E1	1.500	1.700	0.059	0.067	
е	0.950 Typ	pical Value	0.037 Typical Value		
e1	1.900 Typ	oical Value	0.075 Typical Value		
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

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	Dimension (mm)										
Mark	а	В	С	d	E	F	P0	Р	P1	W	
Number											
SOT-23-6	3.17	3.23	1.37	Ф1.55	1.75	3.50	4.00	4.00	2.00	8.00	



Dimension Information (mm)									
Mark Number	D	D1	D2	G	Н	I	W1	W2	
7' Reel	Ф180.00	60.00	13.00	Radius78.00	Radius25.60	Radius6.50	9.50	13.10	

Reel Package	Reel Dimension	Box Package	Box Dimension (mm)	Crate Package	Crate Dimension (mm)
3000 Pieces	7 Inches	30,000 Pieces	203*203*195	120,000 Pieces	438*438*220

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