



To request the full datasheet, please visit www.intersil.com/products/TW8832

TFT Display Controller

TW8832, TW8832S

Description

The TW8832 incorporates many of the features required to create multi-purpose in-car LCD display system in a single package. It integrates a high quality 2D comb NTSC/PAL/SECAM video decoder, triple high speed RGB ADCs, high quality scaler, and images enhancement functions, which include Black and White Stretch, etc. It also supports panoramic scaling for conversion to wide screen display. On the input side, it supports a rich combination of CVBS, S-video, analog RGB as well as digital inputs. On the output side, it supports a variety of digital panel types with its built-in timing controller.

Applications

- In-car display controller
- Portable DVD and DVRs players
- Portable media player

Features

Analog Video Decoder

- NTSC (M, 4.34) and PAL (B, D, G, H, I, M, N, N combination), PAL (60), SECAM with automatic format detection
- Three 10-bit ADCs and analog clamping circuit
- Fully programmable static gain or automatic gain control for the Y or CVBS channel
- Programmable white peak control for the Y or CVBS channel
- Software selectable analog inputs
- High quality adaptive 2D comb filter for both NTSC and PAL inputs
- PAL delay line for color phase error correction
- Image enhancement with 2D dynamic peaking and CTI
- Digital sub-carrier PLL for accurate color decoding
- Digital horizontal PLL and Advanced synchronization processing for VCR playback and weak signal performance
- Programmable hue, brightness, saturation, contrast and sharpness
- High quality horizontal and vertical filtered down scaling with arbitrary scale down ratio

Analog RGB Inputs

- Triple high speed 10-bit ADCs with clamping and programmable gain amplifier
- SOG and H/V sync support for YPbPr or RGB input
- Built-in line locked PLL with sync separator
- Support analog input resolution up to 1080i or WXGA

Digital Inputs Support

- Support 656 video formats

TFT Panel Support

- Built-in programmable timing controller
- Supports 3, 4, 6 or 8 bits per pixel up to 16.8 million colors with built-in dithering engine
- Support digital panel up to WVGA/SVGA resolution
- Support Serial (8-bit) RGB panel

On Screen Display

- 256 programmable font RAM and 384 display RAM
- Four windows font OSD with bordering / shadow
- Supports bit-mapped based OSD through SPI (TW8832S only)

Image Processing

- High quality scaler with both up/down and nonlinear scaling support
- Built-in 2D de-interlacing function
- Programmable hue, brightness, saturation, contrast and sharpness
- Supports programmable cropping of input video and graphics
- Independent RGB gain and offset controls
- Panorama/Water-glass scaling
- Programmable 10-bit Gamma correction for each color
- Operated in Frame Sync mode
- Black/White Stretch

Clock Generation

- Spread spectrum clock
- Modulation frequency and spread width are selectable

TW8832, TW8832S

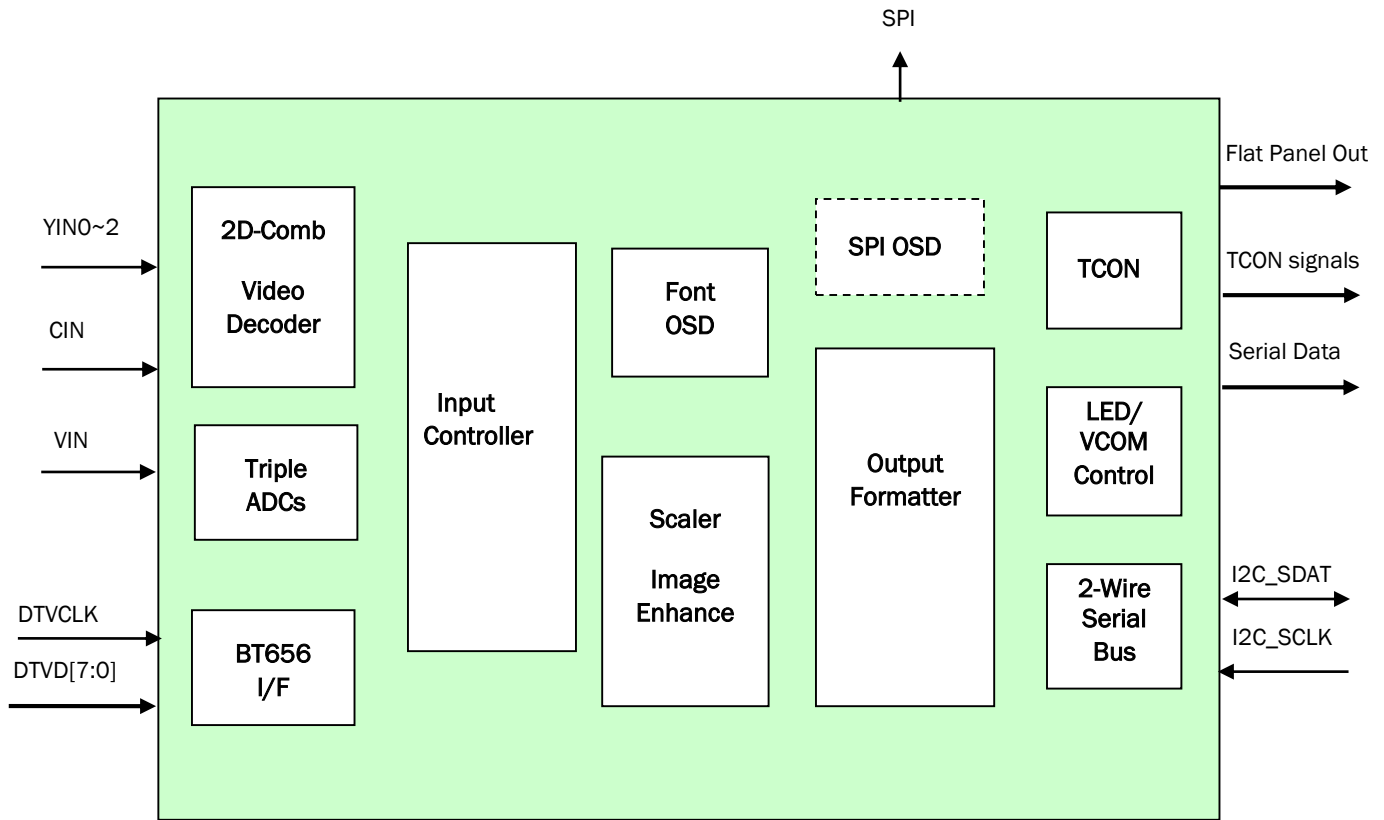


FIGURE 1. FUNCTIONAL BLOCK DIAGRAM

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